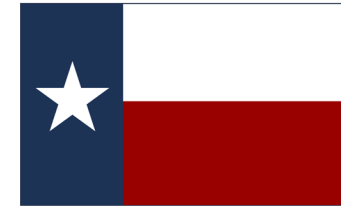


PDF PAGE	CSA PAGE	CONTENTS	SYNC MASTER	DATE
2	2	H5P JTAG, USB ,PLL	N/A	N/A
3	3	H5P GPIO & CONTROL	N/A	N/A
4	4	H5P IO POWER	N/A	N/A
5	5	H5P SOC/CPU/SRAM PWR	N/A	N/A
6	6	H5P W/ NAND	N/A	N/A
7	7	H5P VIDEO	N/A	N/A
8	8	BUTTON FLEX B2B	N/A	N/A
9	9	L81 AUDIO CODEC	N/A	N/A
10	10	CG FLEX B2B	N/A	N/A
11	12	AGATHA PMU(1/2)	N/A	N/A
12	13	AGATHA PMU(2/2)	N/A	N/A
13	14	CHESTNUT + BACKLIGHT DRIVER	N/A	N/A
14	15	SPKR AMP + LED DRIVER	N/A	N/A
15	16	TRISTAR	N/A	N/A
16	17	DOCKFLEX B2B	N/A	N/A
17	18	D404 (TOUCH B2B, DRIVER ICS)	N/A	N/A
18	19	LCM CONNECTOR	N/A	N/A
19	20	OSCAR + SENSORS	N/A	N/A
20	21	CAM0 CONNECTOR	N/A	N/A
21	22	BATT B2B, TPS, PD FEATURES	N/A	N/A
22	23	RADIO_MLB HIERARCH. SYMBOL	N/A	N/A

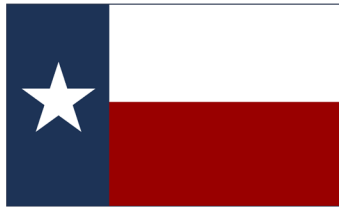


iPhone 5C Schematics

Dewatermarked &
Released Free
From Texas with Love.

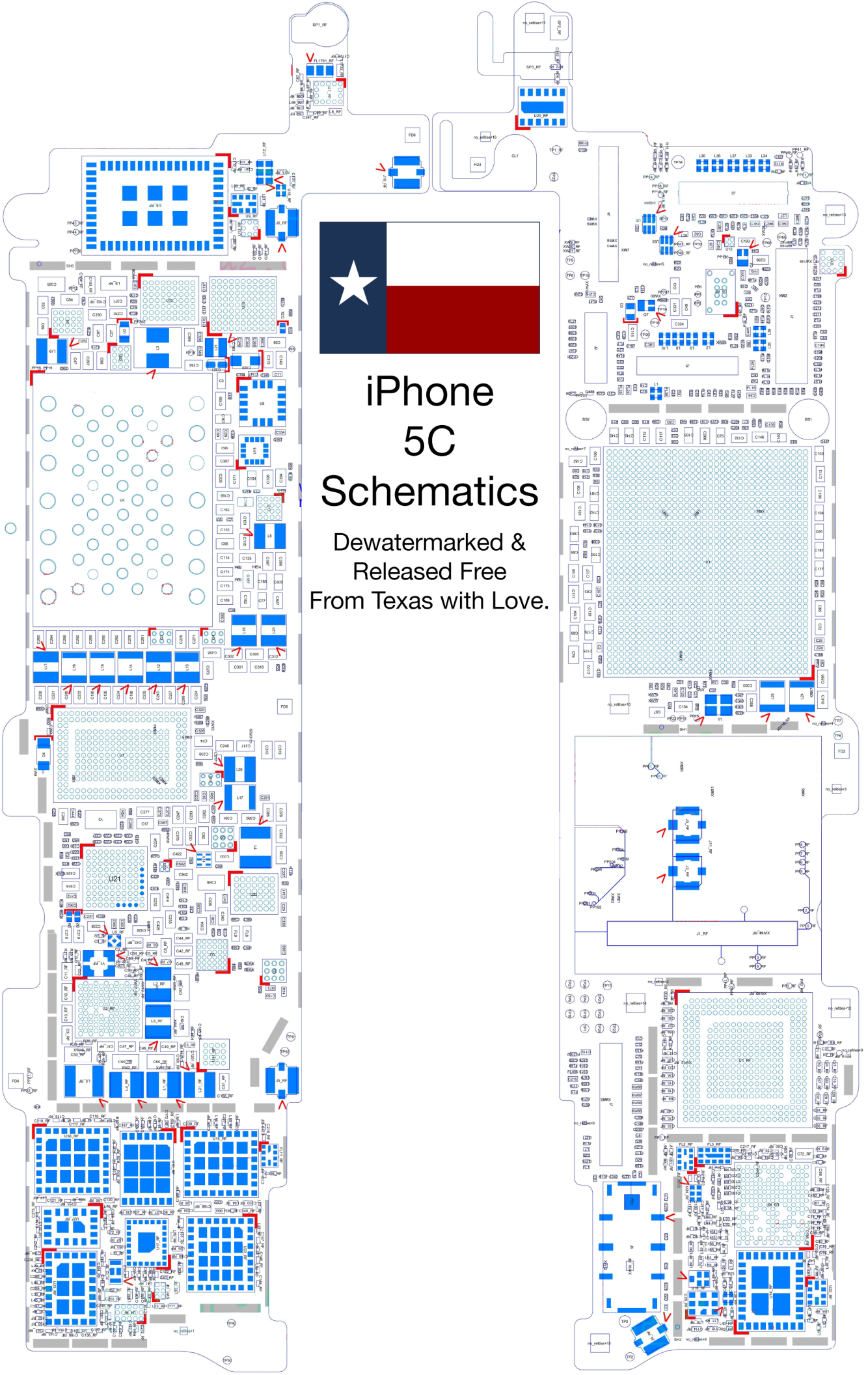
SCH 051-0143
BRD 820-3581

BOM 639-4501

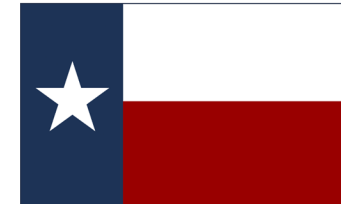


iPhone 5C Schematics

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PDF PAGE	CSA PAGE	CONTENTS	SYNC MASTER	DATE
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6	6	H5P W/ NAND	N/A	N/A
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8	8	BUTTON FLEX B2B	N/A	N/A
9	9	L81 AUDIO CODEC	N/A	N/A
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11	12	AGATHA PMU(1/2)	N/A	N/A
12	13	AGATHA PMU(2/2)	N/A	N/A
13	14	CHESTNUT + BACKLIGHT DRIVER	N/A	N/A
14	15	SPKR AMP + LED DRIVER	N/A	N/A
15	16	TRISTAR	N/A	N/A
16	17	DOCKFLEX B2B	N/A	N/A
17	18	D404 (TOUCH B2B, DRIVER ICS)	N/A	N/A
18	19	LCM CONNECTOR	N/A	N/A
19	20	OSCAR + SENSORS	N/A	N/A
20	21	CAM0 CONNECTOR	N/A	N/A
21	22	BATT B2B, TPS, PD FEATURES	N/A	N/A
22	23	RADIO_MLB HIERARCH. SYMBOL	N/A	N/A



iPhone 5C Schematics

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Released Free
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SCH 051-0143
BRD 820-3581

BOM 639-4501

CG FLEX B2B

(FF CAM, PROX, ALS, RECEIVER, ANC ERROR MIC)

FRONT CAM:
CLK, I2C, SHDN

PROX: POWER,
RX, RX_EN

ALS: POWER,
I2C, INT

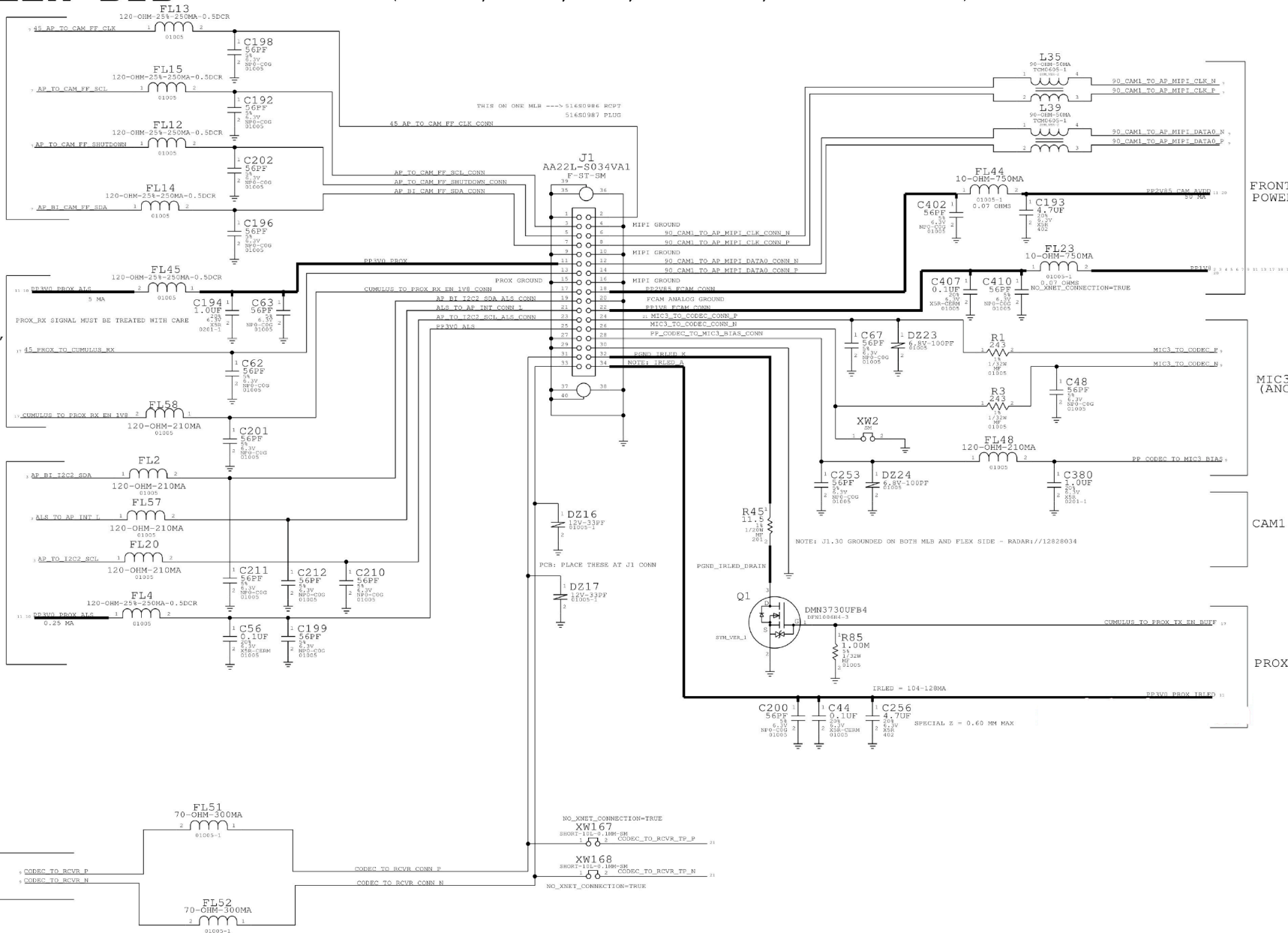
RECEIVER

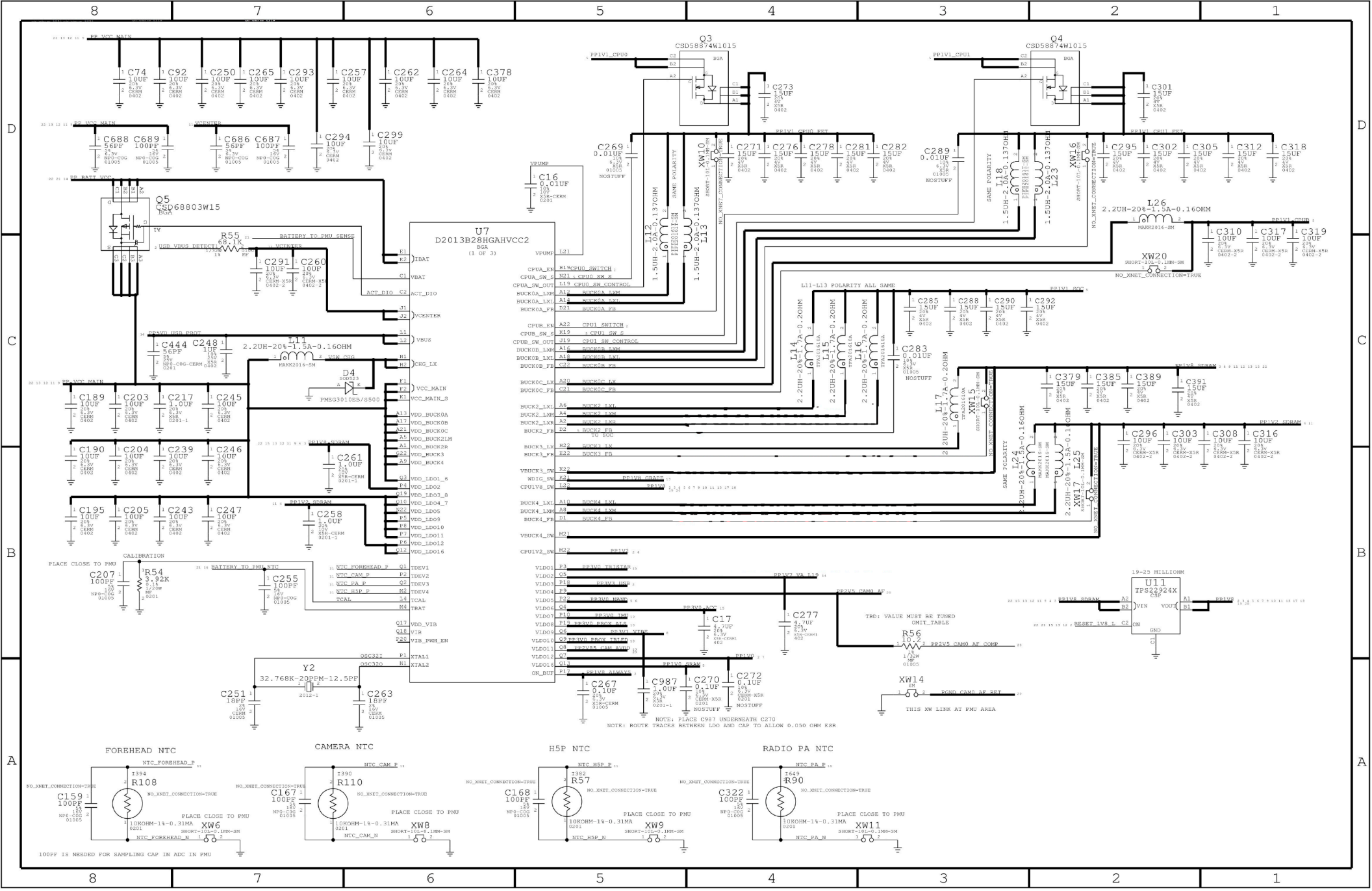
FRONT CAM:
POWER AND MIPI

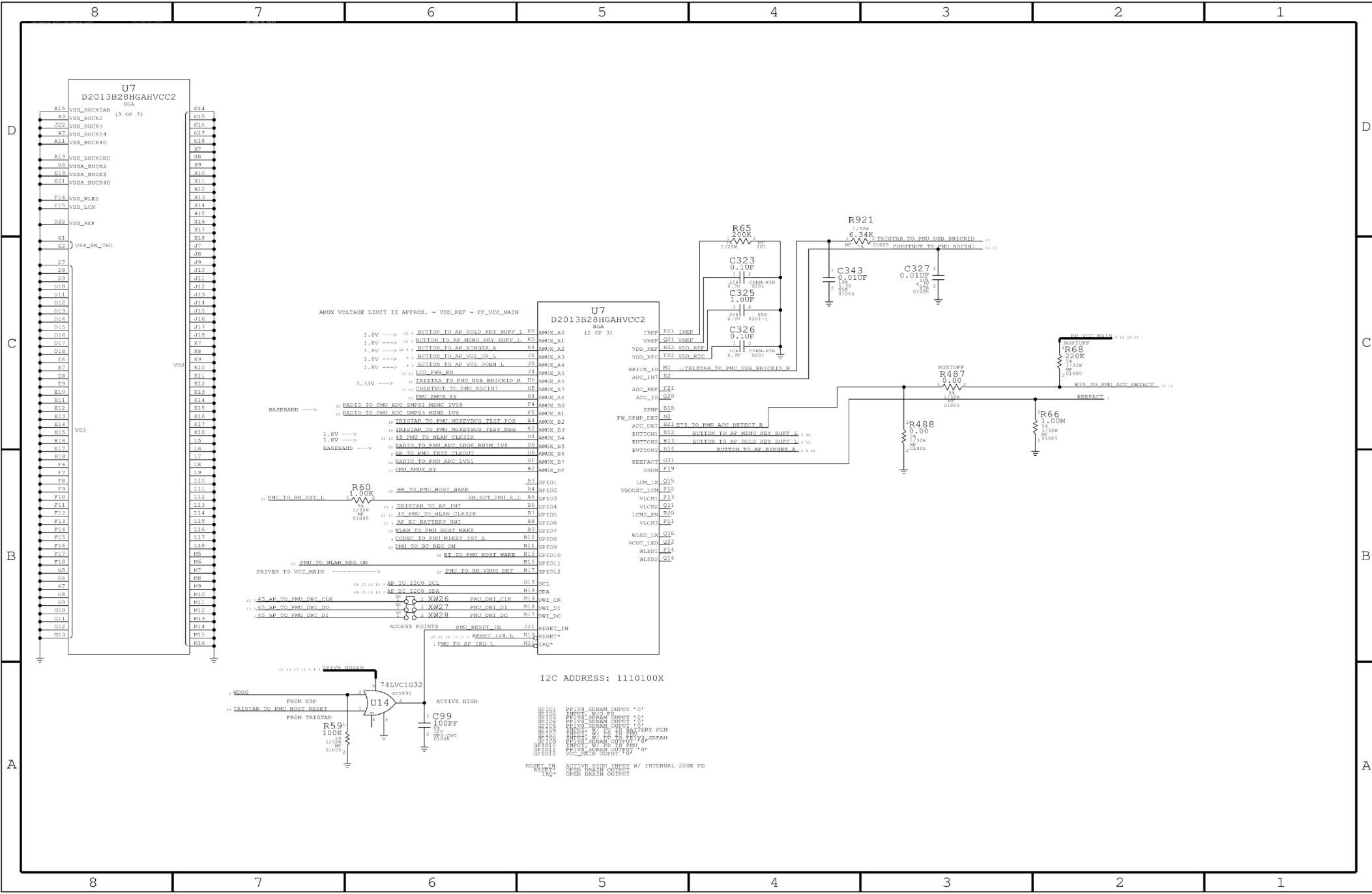
MIC3
(ANC ERROR MIC)

CAM1 ALS INT

PROX: PWR, TX EN

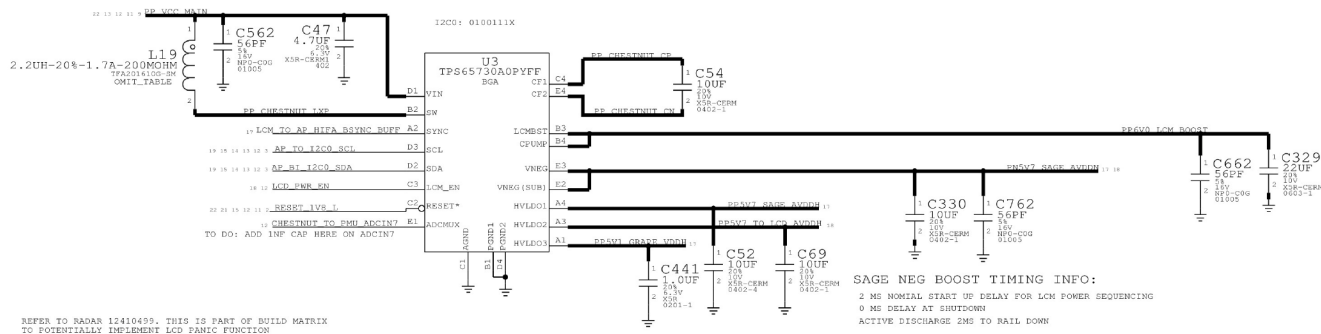




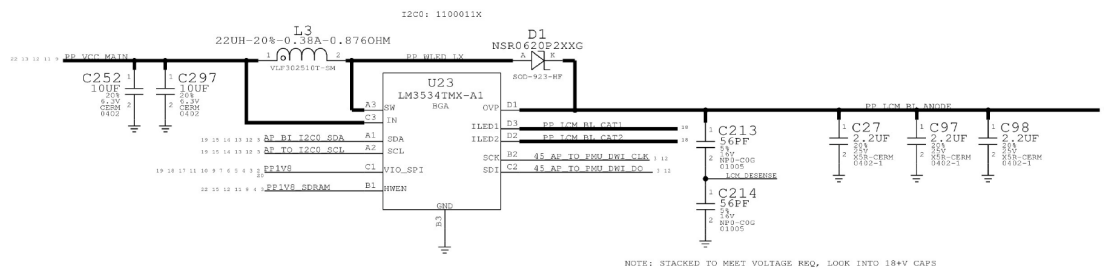


CHESTNUT, BACKLIGHT DRIVER

D404 DISPLAY PMU (INTERSIL CHESTNUT, 338S1168)
(TI CHESTNUT, 338S1172)



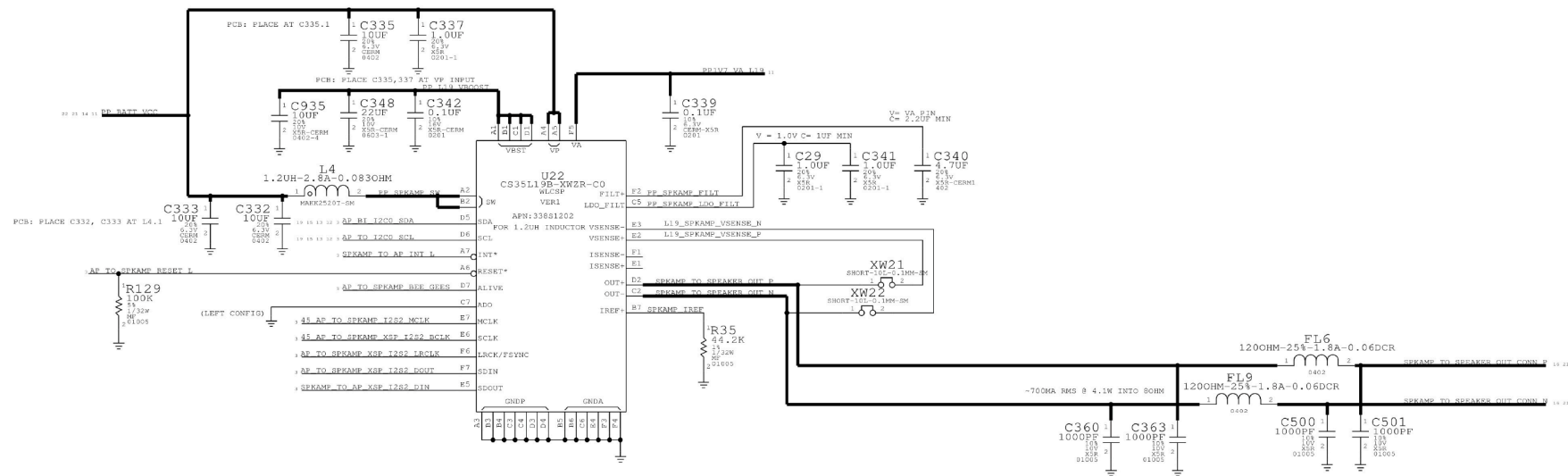
D404 BACKLIGHT DRIVER



SPEAKER AMP, LED DRIVER

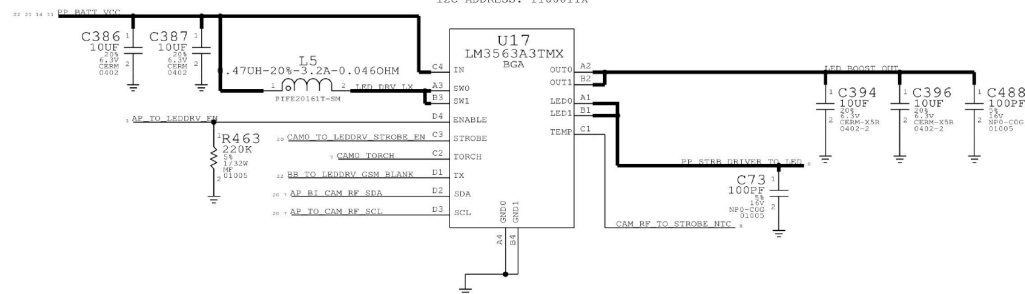
SPEAKER AMP L19

I2C ADDRESS: 1000000X



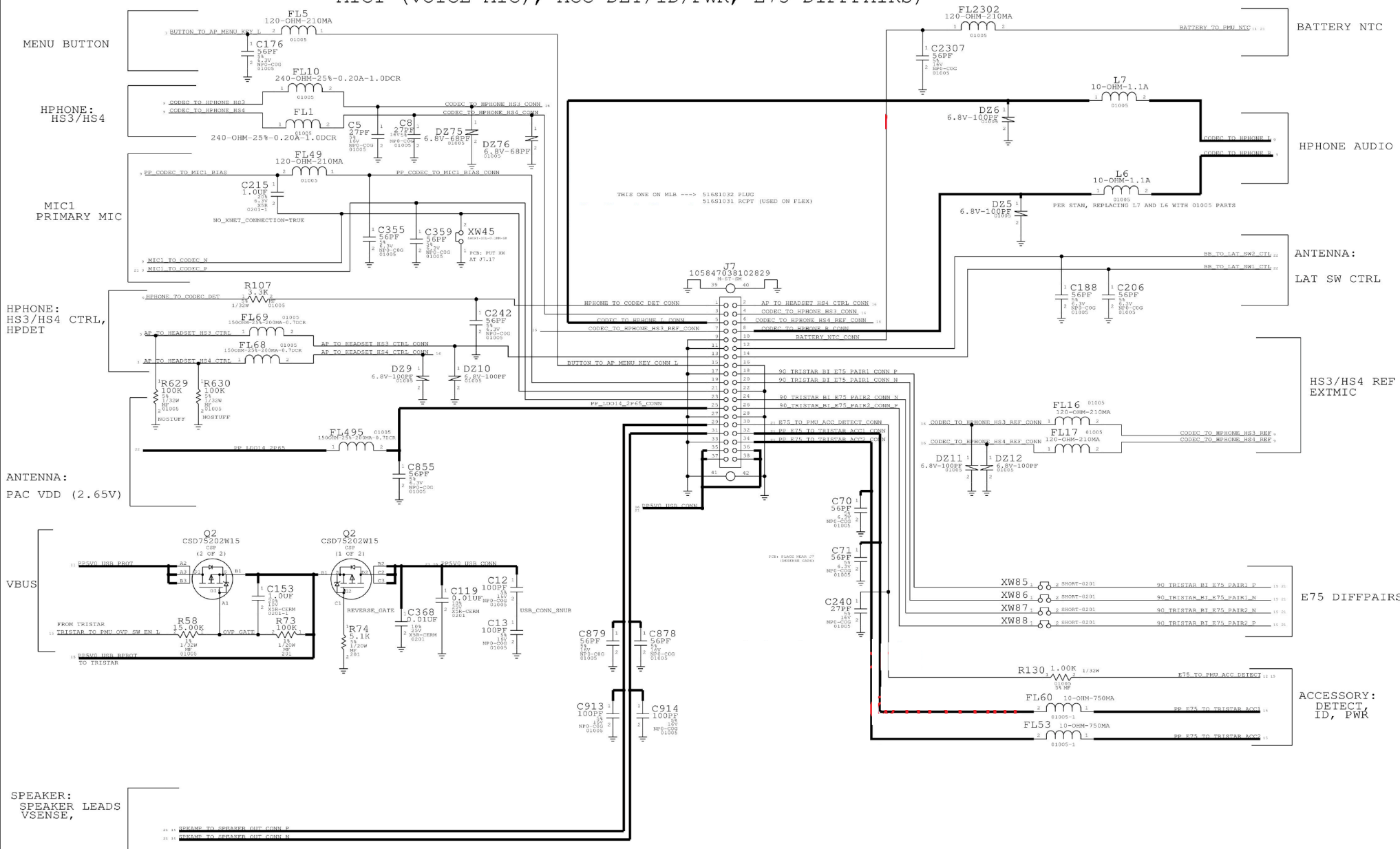
LED DRIVER

I2C ADDRESS: 1100011X



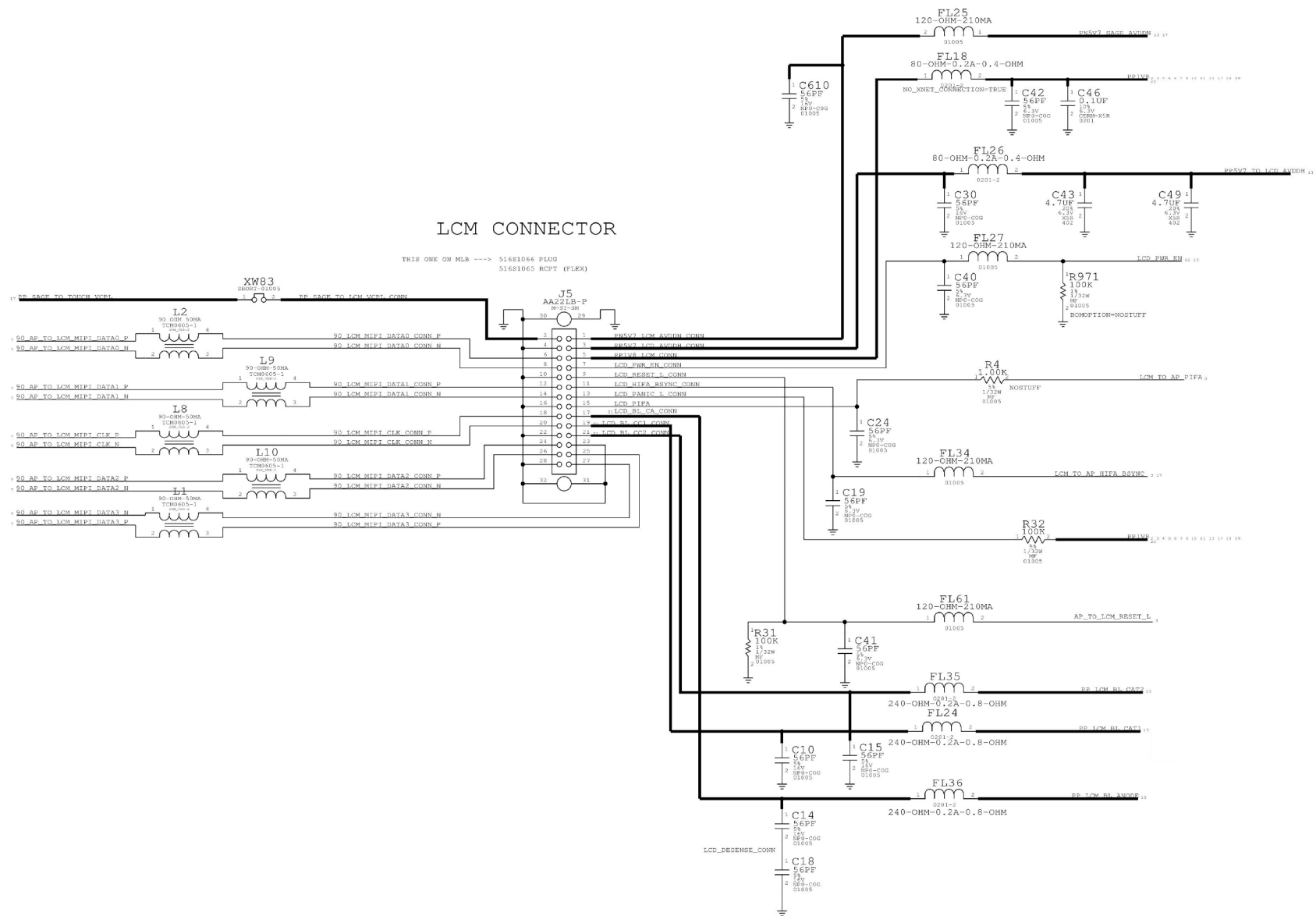
[illegible]

DOCKFLEX B2B (USB VBUS, MENU BTN, SPEAKER, HP, HP EXTMIC, NAVAJO, ANTENNA PAC/LAT SW CTRL, MIC1 (VOICE MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)



LCM B2B

LCM CONNECTOR

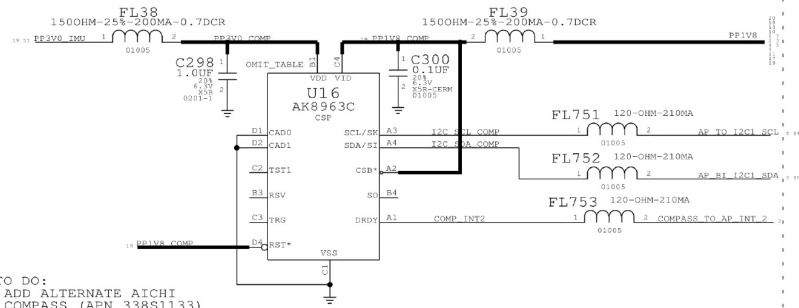


SENSORS

THIS PART OUTSIDE OF SHIELD

COMPASS

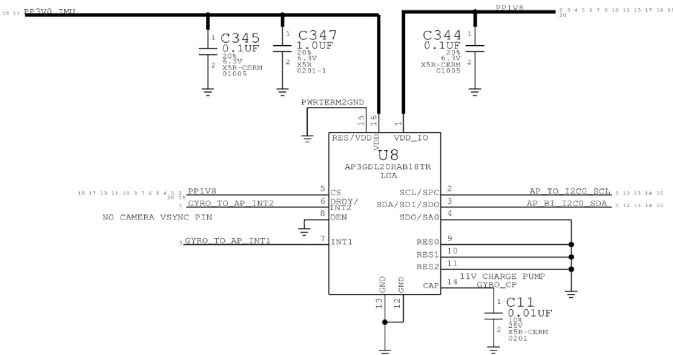
COMPASS DEVICE: 338S1014
COMPASS INTERPOSER: 998-5120



THESE PARTS INSIDE OF SHIELD

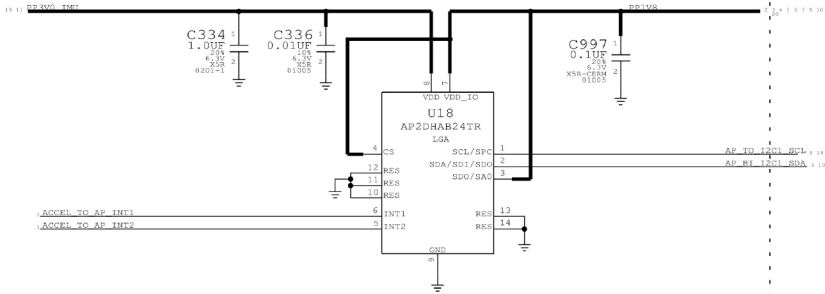
GYRO

AP3GDL20HAB, APN 338S1192

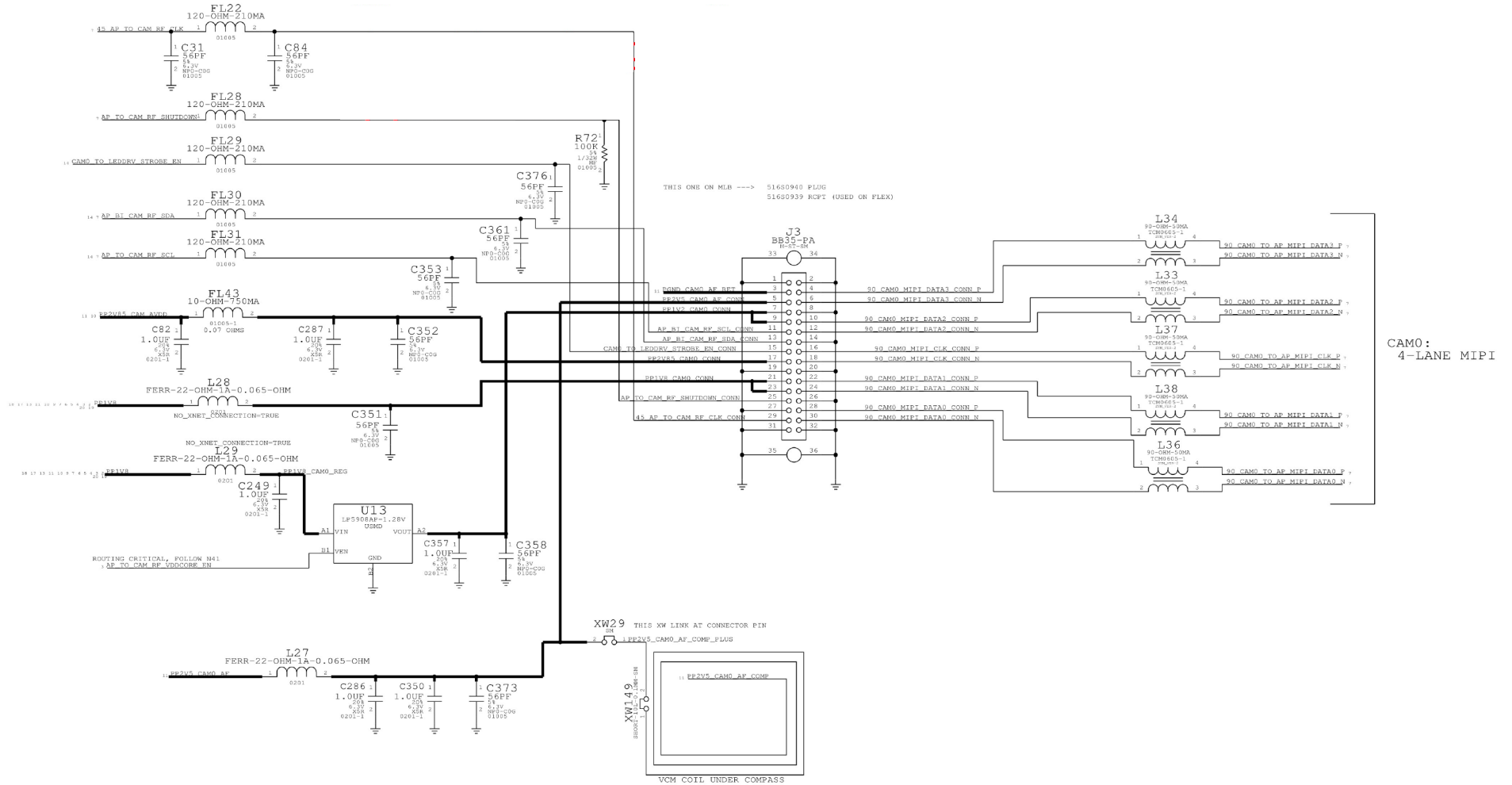


ACCELEROMETER

AP2DHAB, APN 338S1191

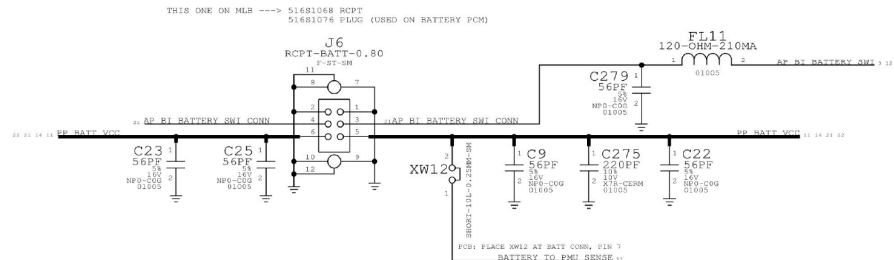


CAM0: MAIN CAMERA CONNECTOR

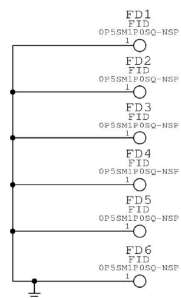


BATT CONN, TPS, STANDOFFS/SHIELDS/FIDUCIALS

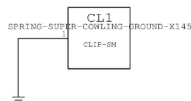
BATTERY CONN



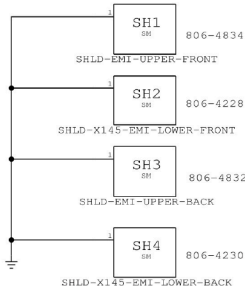
FIDUCIALS



COWLING UPPER LEFT

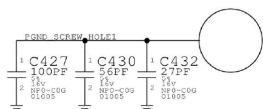


SHIELDS

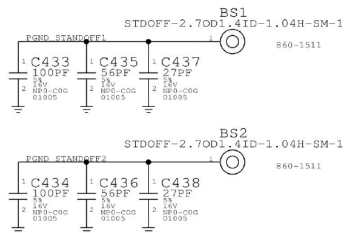


AC COUPLED SCREW HOLES + STANDOFFS (ON NORTH END OF SINGLE_BSD, TO MITIGATE COMPASS RETURN CURRENTS)

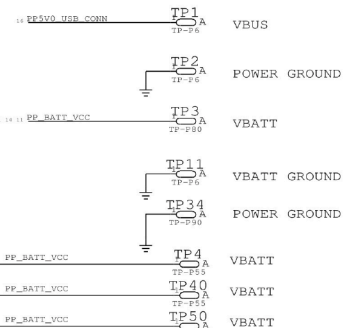
SCREW HOLES



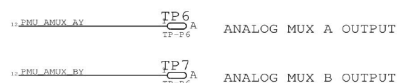
STANDOFFS



POWER TP



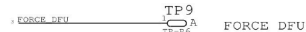
SUPER TP



RESET

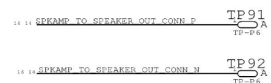


DFU

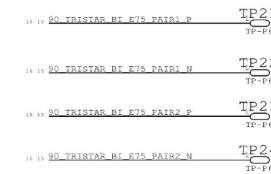


TESTPOINTS

SPKAMP OUTPUT TP



E75 - USB/UART/ID/POWER



ACCESSORY ID AND POWER



POWER GROUND



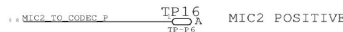
POWER GROUND



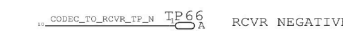
FOR DIAGS



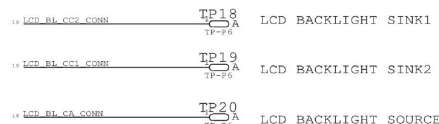
MIC AUDIO



DRIVE MIC WRT NEAREST GROUND TEST POINT

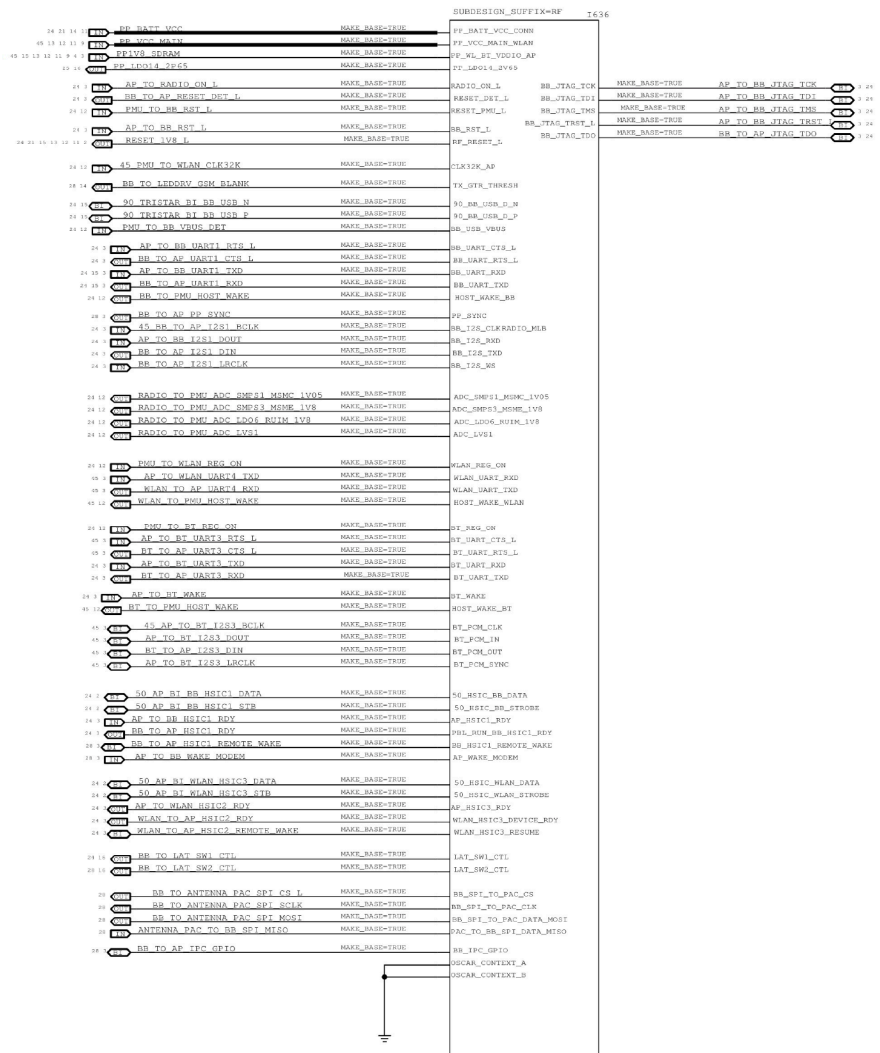


LCM BACKLIGHT



RADIO_MLB HIERARCHICAL SYMBOL

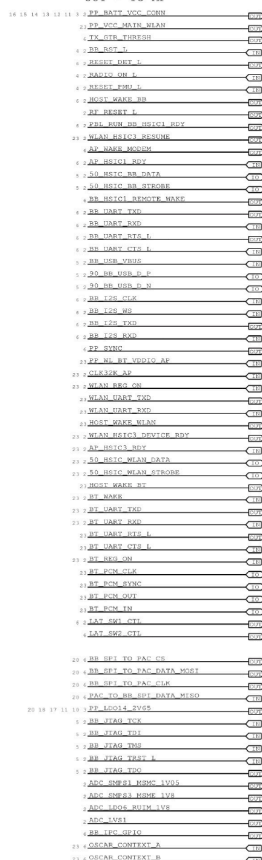
AP/RADIO INTERFACE



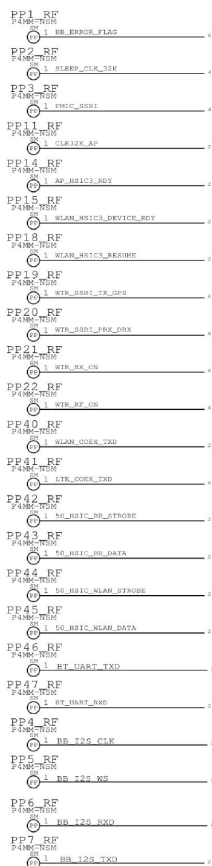
AP INTERFACE & DEBUG CONNECTORS

AP CONNECTIONS

IN = FROM AP
OUT = TO AP



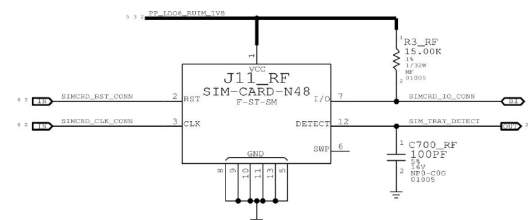
PROBE POINTS



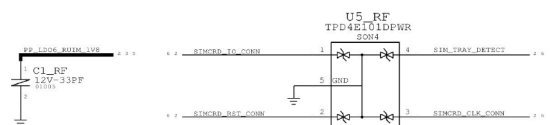
PROBE POINTS NEAR AP



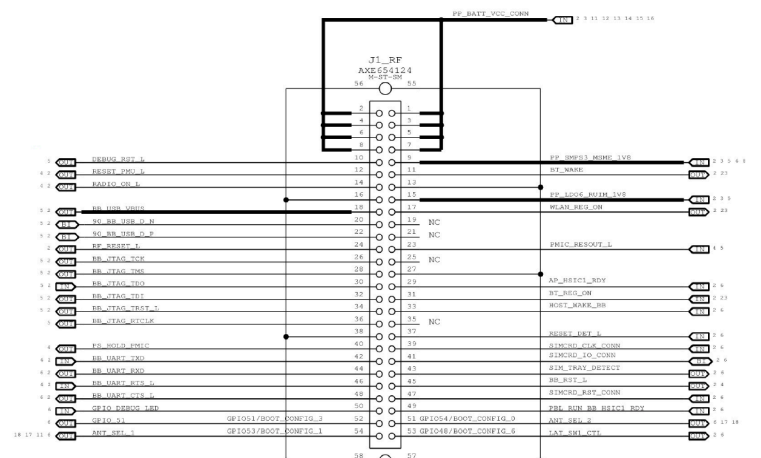
SIM CARD CONNECTOR



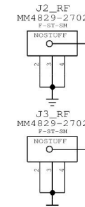
SIM CARD ESD PROTECTION



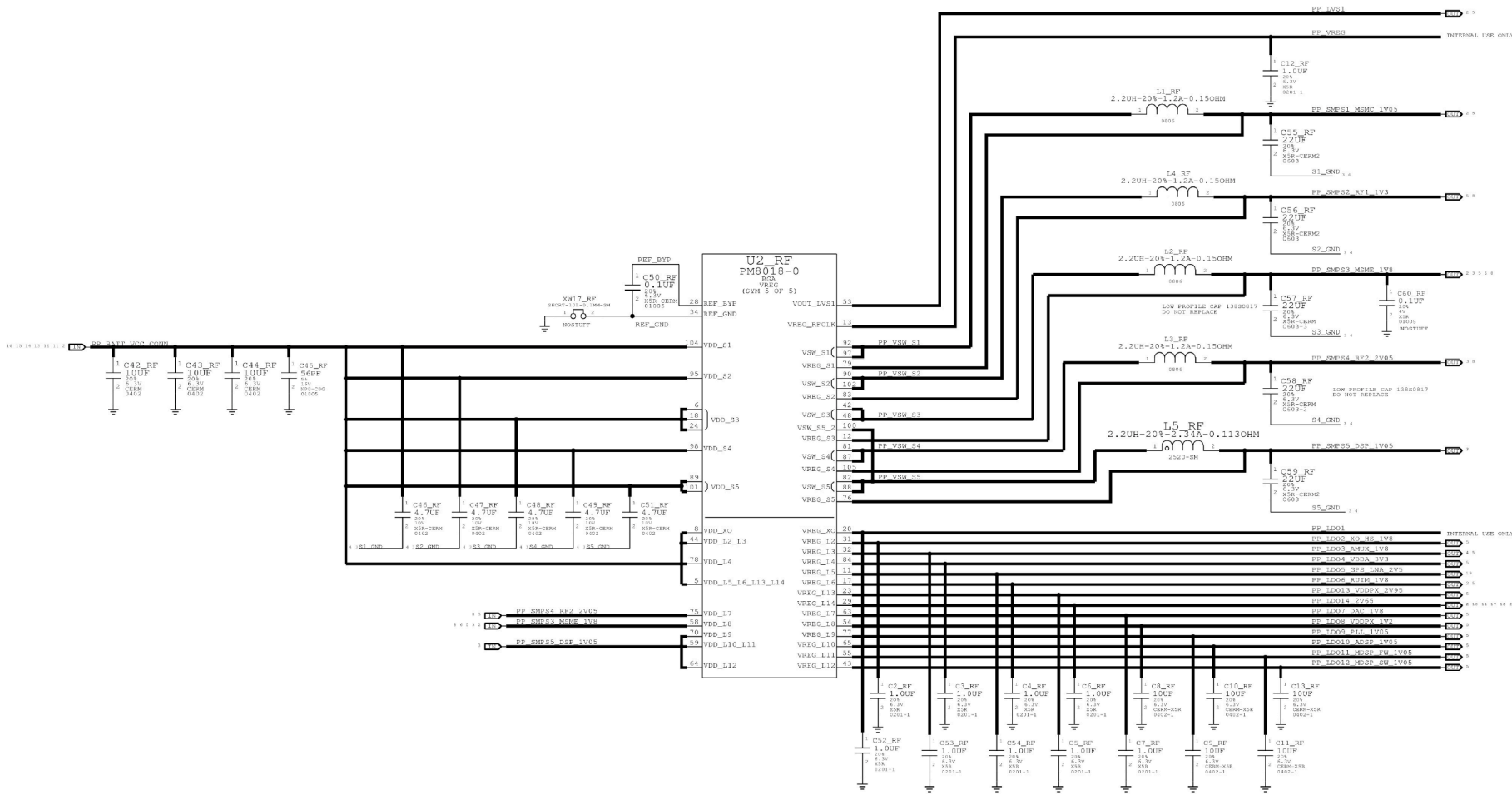
DEBUG CONNECTOR



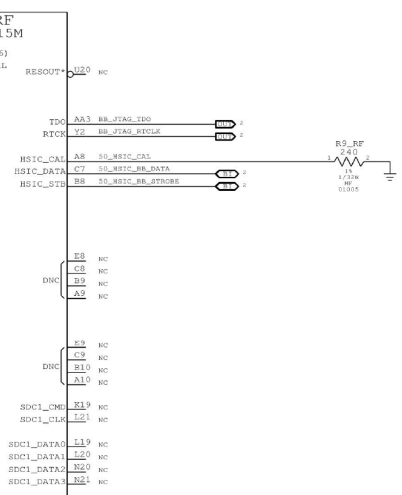
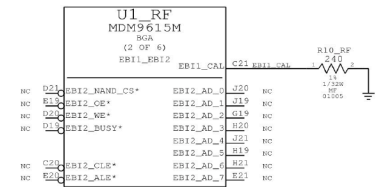
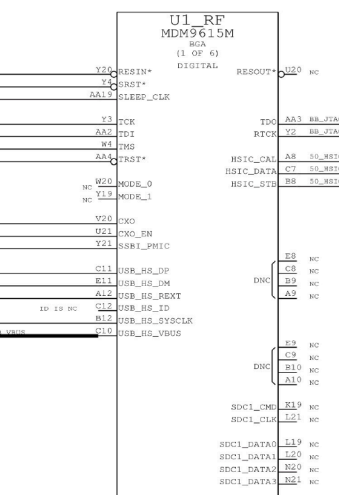
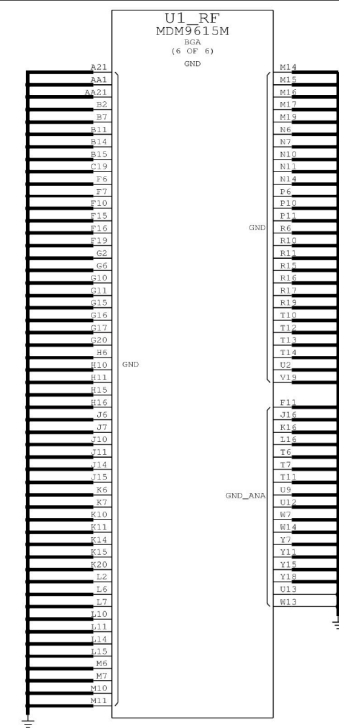
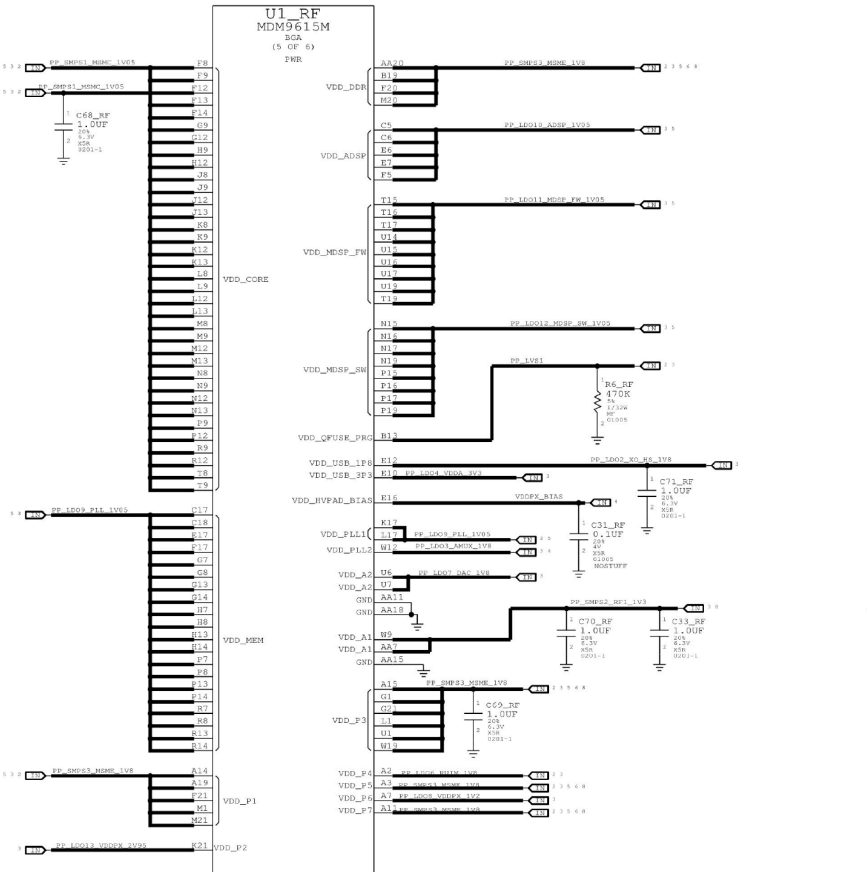
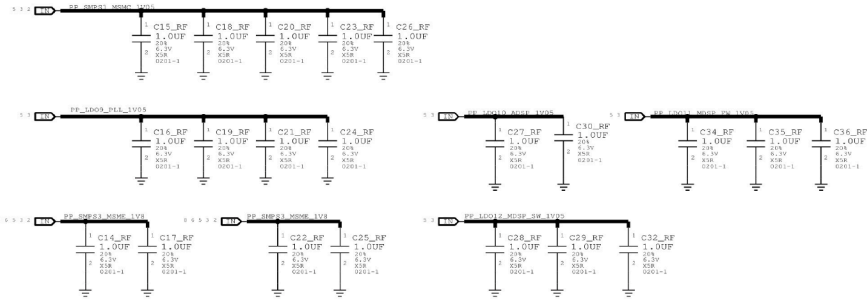
ROOT OPTIONS	ROOT_CONFIG SM REGISTER VALUE	SPIN/BOOT_OPTION CONFIGURATION							
		47	48	49	50	51	52	53	54
ROOT_DEFAULT_OPTION	0x00	x	x	x	x	x	x	x	x
ROOT_MERGE_OPTION	0x01	x	x	x	x	x	x	x	x
ROOT_BOOT_OPTION	0x02	x	x	x	x	x	x	x	x
ROOT_BOOT_OPTION	0x03	x	x	x	x	x	x	x	x
CHANGE SANDBOX PROTOCOL	0x04	x	x	x	x	x	x	x	x



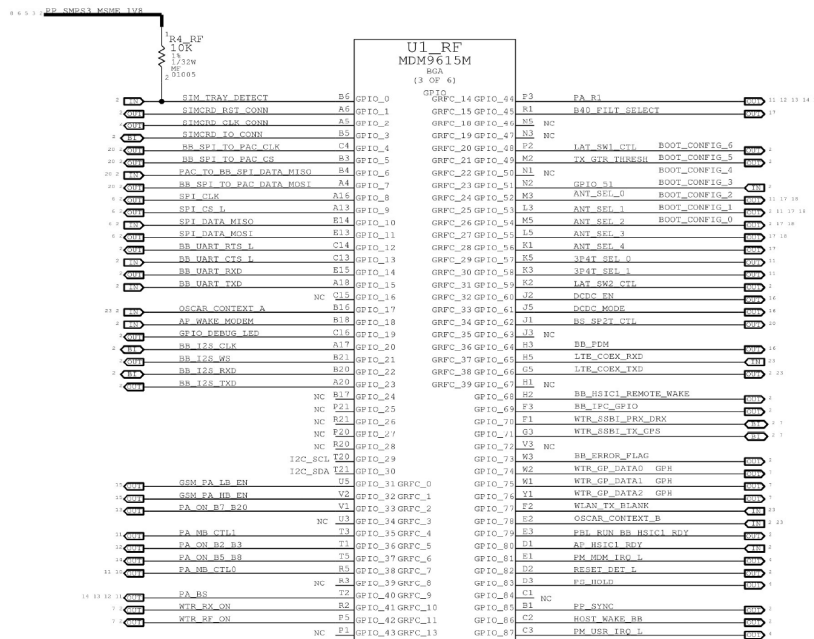
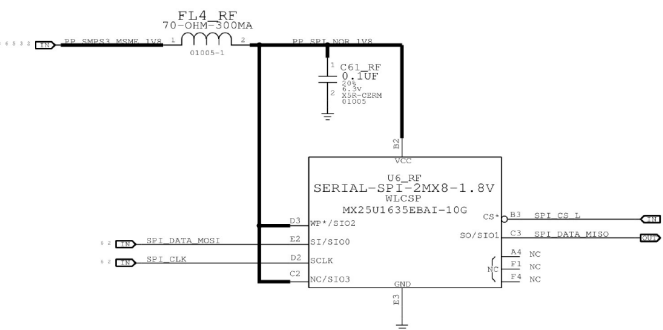
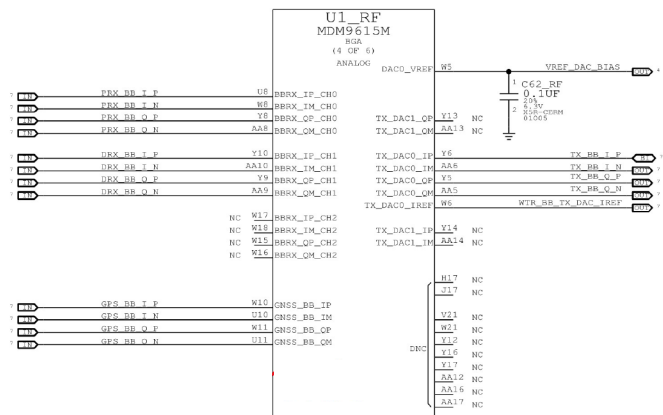
PMU (1 OF 2)



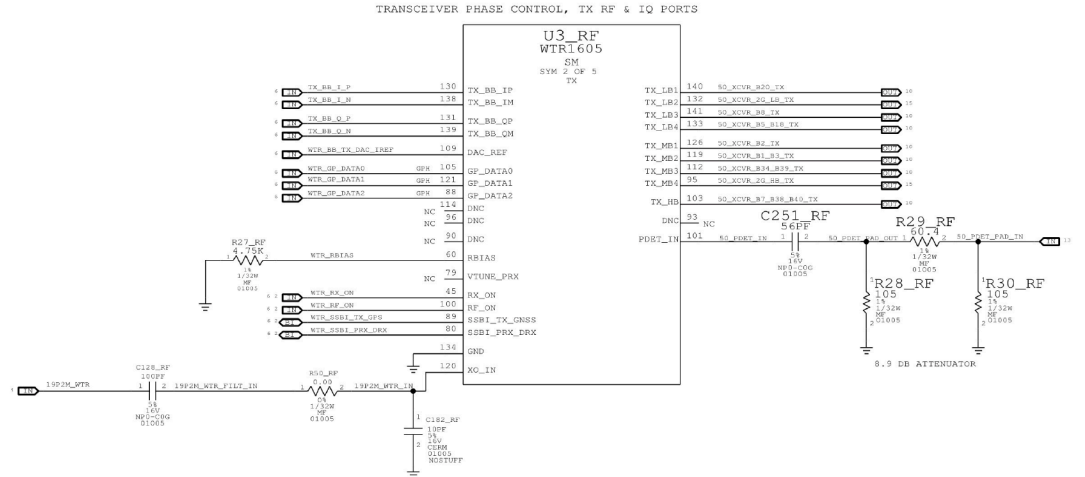
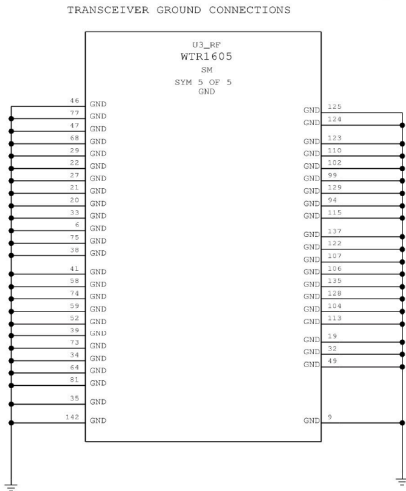
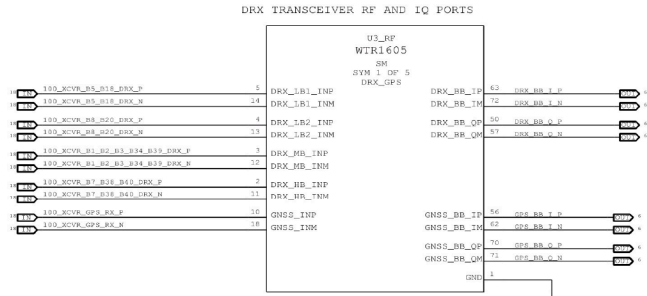
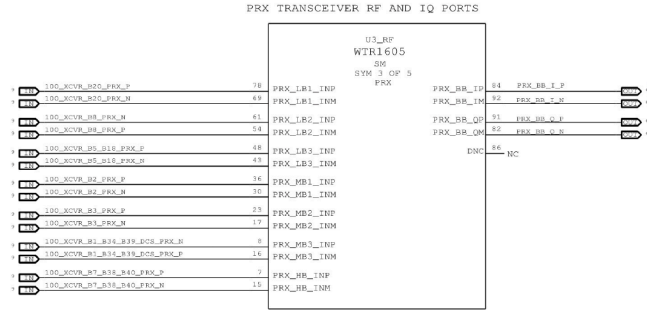
BASEBAND (1 OF 2)

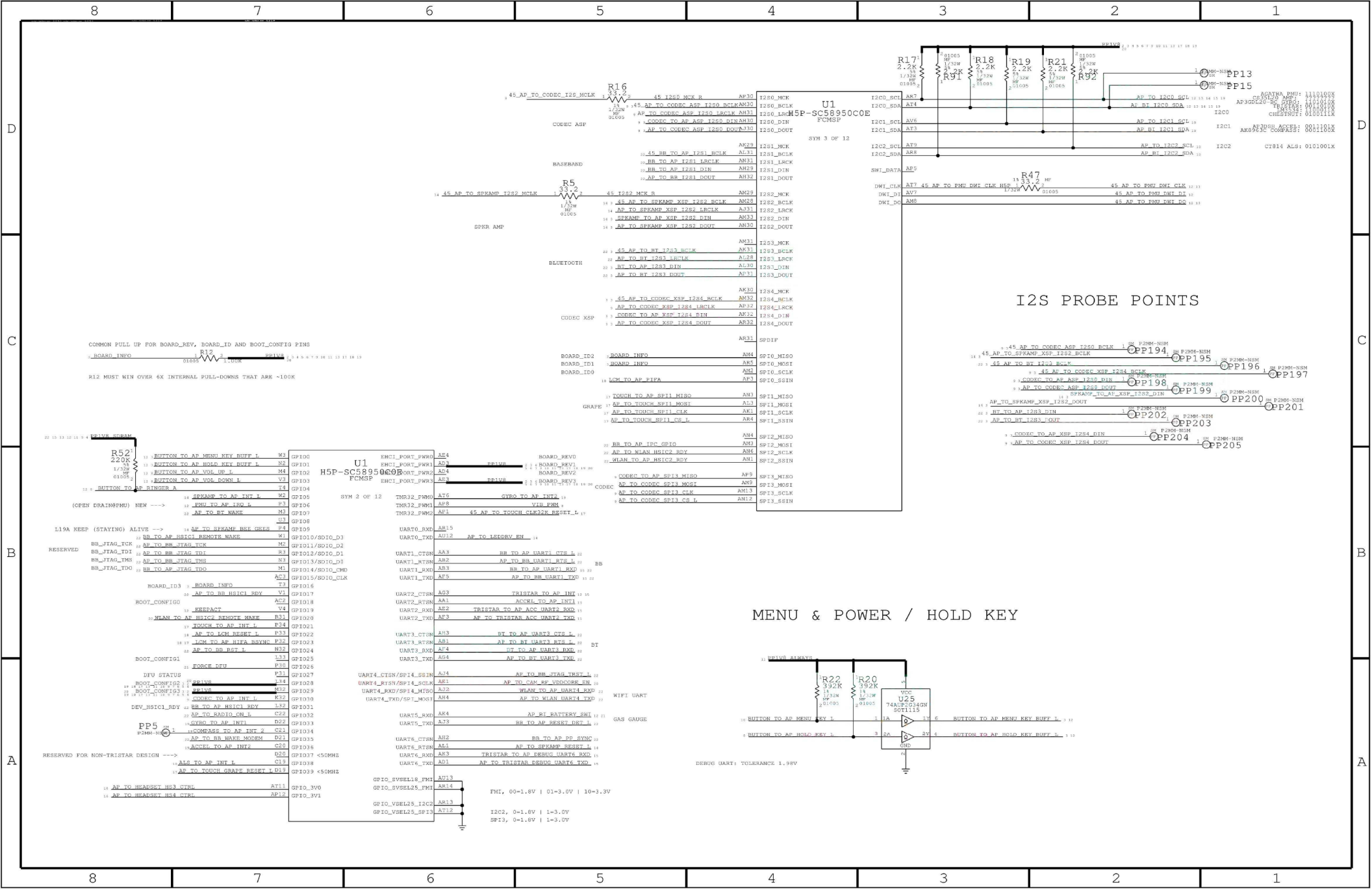


BASEBAND (2 OF 2)

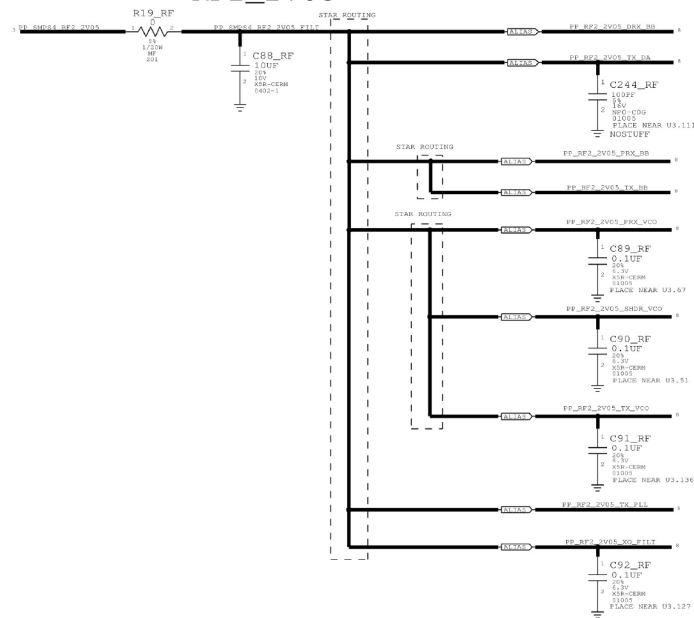
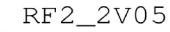
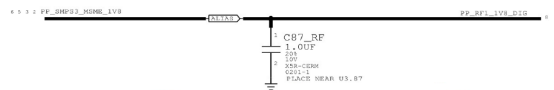
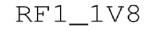
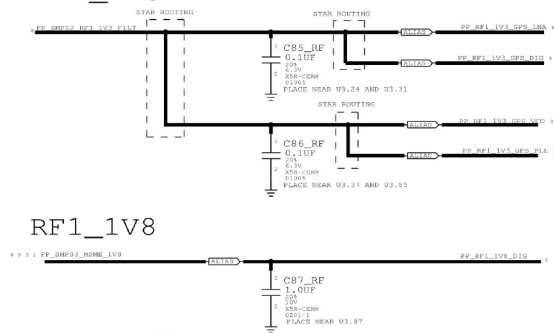
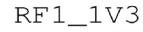
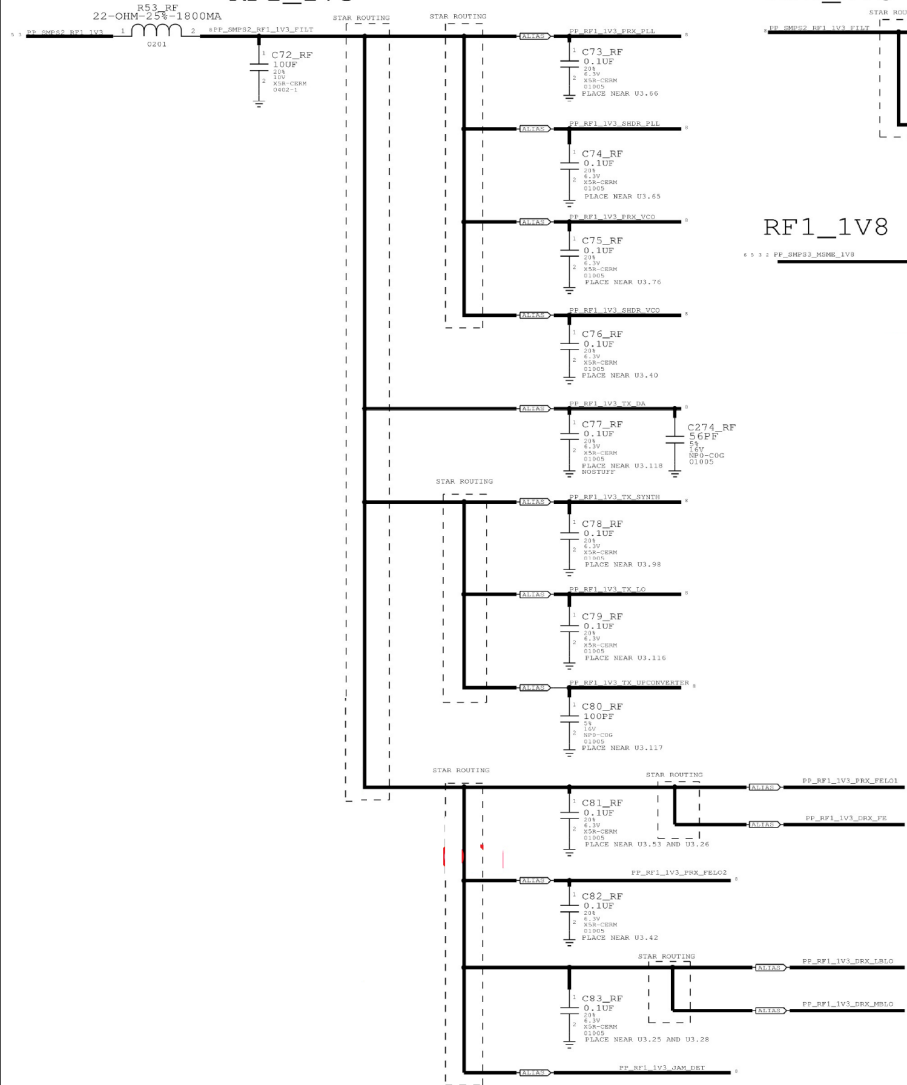
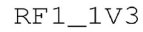


RF TRANSCEIVER (1 OF 2)





RF TRANSCEIVER (2 OF 2)



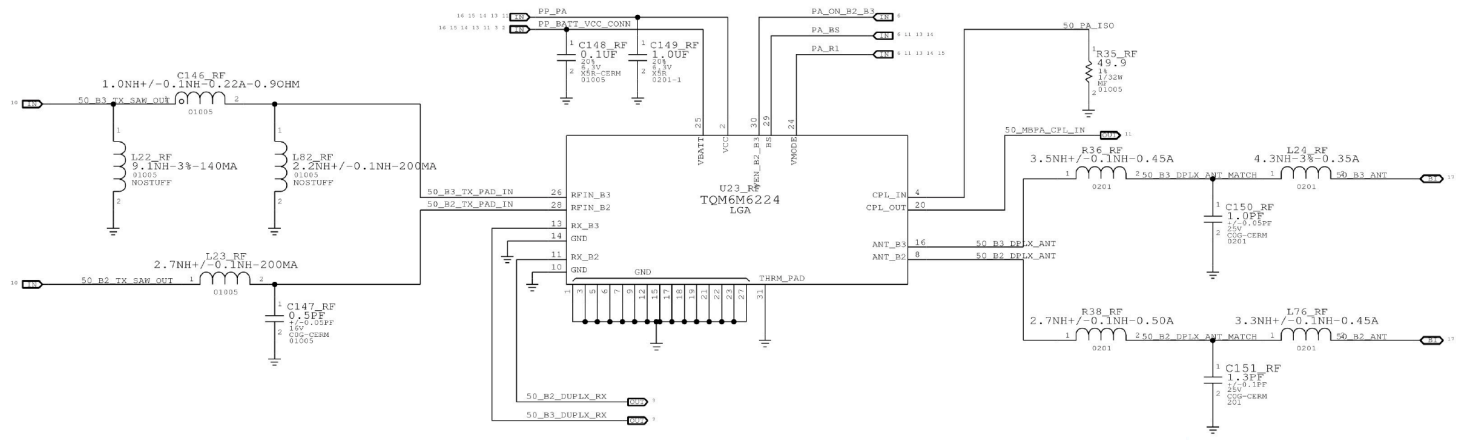
TRANSCEIVER POWER CONNECTIONS

		U3_RF		WTR1605	
				SM	
		SIM 4 of 5			
		FMR			
FE_RF2_101_F00_F180	52	VDD_RF1_P_FRI0	111	FE_RF2_1005_F00_F00	111
FE_RF2_101_F00_F180	53	VDD_RF1_P_FRI0	112	FE_RF2_1005_F00_F00	112
FE_RF2_101_F00_F180	54	VDD_RF1_P_FRI0	113	FE_RF2_1005_F00_F00	113
FE_RF2_101_F00_F180	55	VDD_RF1_P_FRI0	114	FE_RF2_1005_F00_F00	114
FE_RF2_101_F00_F180	56	VDD_RF1_P_FRI0	115	FE_RF2_1005_F00_F00	115
FE_RF2_101_F00_F180	57	VDD_RF1_P_FRI0	116	FE_RF2_1005_F00_F00	116
FE_RF2_101_F00_F180	58	VDD_RF1_P_FRI0	117	FE_RF2_1005_F00_F00	117
FE_RF2_101_F00_F180	59	VDD_RF1_P_FRI0	118	FE_RF2_1005_F00_F00	118
FE_RF2_101_F00_F180	60	VDD_RF1_P_FRI0	119	FE_RF2_1005_F00_F00	119
FE_RF2_101_F00_F180	61	VDD_RF1_P_FRI0	120	FE_RF2_1005_F00_F00	120
FE_RF2_101_F00_F180	62	VDD_RF1_P_FRI0	121	FE_RF2_1005_F00_F00	121
FE_RF2_101_F00_F180	63	VDD_RF1_P_FRI0	122	FE_RF2_1005_F00_F00	122
FE_RF2_101_F00_F180	64	VDD_RF1_P_FRI0	123	FE_RF2_1005_F00_F00	123
FE_RF2_101_F00_F180	65	VDD_RF1_P_FRI0	124	FE_RF2_1005_F00_F00	124
FE_RF2_101_F00_F180	66	VDD_RF1_P_FRI0	125	FE_RF2_1005_F00_F00	125
FE_RF2_101_F00_F180	67	VDD_RF1_P_FRI0	126	FE_RF2_1005_F00_F00	126
FE_RF2_101_F00_F180	68	VDD_RF1_P_FRI0	127	FE_RF2_1005_F00_F00	127
FE_RF2_101_F00_F180	69	VDD_RF1_P_FRI0	128	FE_RF2_1005_F00_F00	128
FE_RF2_101_F00_F180	70	VDD_RF1_P_FRI0	129	FE_RF2_1005_F00_F00	129
FE_RF2_101_F00_F180	71	VDD_RF1_P_FRI0	130	FE_RF2_1005_F00_F00	130
FE_RF2_101_F00_F180	72	VDD_RF1_P_FRI0	131	FE_RF2_1005_F00_F00	131
FE_RF2_101_F00_F180	73	VDD_RF1_P_FRI0	132	FE_RF2_1005_F00_F00	132
FE_RF2_101_F00_F180	74	VDD_RF1_P_FRI0	133	FE_RF2_1005_F00_F00	133
FE_RF2_101_F00_F180	75	VDD_RF1_P_FRI0	134	FE_RF2_1005_F00_F00	134
FE_RF2_101_F00_F180	76	VDD_RF1_P_FRI0	135	FE_RF2_1005_F00_F00	135
FE_RF2_101_F00_F180	77	VDD_RF1_P_FRI0	136	FE_RF2_1005_F00_F00	136
FE_RF2_101_F00_F180	78	VDD_RF1_P_FRI0	137	FE_RF2_1005_F00_F00	137
FE_RF2_101_F00_F180	79	VDD_RF1_P_FRI0	138	FE_RF2_1005_F00_F00	138
FE_RF2_101_F00_F180	80	VDD_RF1_P_FRI0	139	FE_RF2_1005_F00_F00	139
FE_RF2_101_F00_F180	81	VDD_RF1_P_FRI0	140	FE_RF2_1005_F00_F00	140
FE_RF2_101_F00_F180	82	VDD_RF1_P_FRI0	141	FE_RF2_1005_F00_F00	141
FE_RF2_101_F00_F180	83	VDD_RF1_P_FRI0	142	FE_RF2_1005_F00_F00	142
FE_RF2_101_F00_F180	84	VDD_RF1_P_FRI0	143	FE_RF2_1005_F00_F00	143
FE_RF2_101_F00_F180	85	VDD_RF1_P_FRI0	144	FE_RF2_1005_F00_F00	144
FE_RF2_101_F00_F180	86	VDD_RF1_P_FRI0	145	FE_RF2_1005_F00_F00	145
FE_RF2_101_F00_F180	87	VDD_RF1_P_FRI0	146	FE_RF2_1005_F00_F00	146
FE_RF2_101_F00_F180	88	VDD_RF1_P_FRI0	147	FE_RF2_1005_F00_F00	147
FE_RF2_101_F00_F180	89	VDD_RF1_P_FRI0	148	FE_RF2_1005_F00_F00	148
FE_RF2_101_F00_F180	90	VDD_RF1_P_FRI0	149	FE_RF2_1005_F00_F00	149
FE_RF2_101_F00_F180	91	VDD_RF1_P_FRI0	150	FE_RF2_1005_F00_F00	150
FE_RF2_101_F00_F180	92	VDD_RF1_P_FRI0	151	FE_RF2_1005_F00_F00	151
FE_RF2_101_F00_F180	93	VDD_RF1_P_FRI0	152	FE_RF2_1005_F00_F00	152
FE_RF2_101_F00_F180	94	VDD_RF1_P_FRI0	153	FE_RF2_1005_F00_F00	153
FE_RF2_101_F00_F180	95	VDD_RF1_P_FRI0	154	FE_RF2_1005_F00_F00	154
FE_RF2_101_F00_F180	96	VDD_RF1_P_FRI0	155	FE_RF2_1005_F00_F00	155
FE_RF2_101_F00_F180	97	VDD_RF1_P_FRI0	156	FE_RF2_1005_F00_F00	156
FE_RF2_101_F00_F180	98	VDD_RF1_P_FRI0	157	FE_RF2_1005_F00_F00	157
FE_RF2_101_F00_F180	99	VDD_RF1_P_FRI0	158	FE_RF2_1005_F00_F00	158
FE_RF2_101_F00_F180	100	VDD_RF1_P_FRI0	159	FE_RF2_1005_F00_F00	159
FE_RF2_101_F00_F180	101	VDD_RF1_P_FRI0	160	FE_RF2_1005_F00_F00	160
FE_RF2_101_F00_F180	102	VDD_RF1_P_FRI0	161	FE_RF2_1005_F00_F00	161
FE_RF2_101_F00_F180	103	VDD_RF1_P_FRI0	162	FE_RF2_1005_F00_F00	162
FE_RF2_101_F00_F180	104	VDD_RF1_P_FRI0	163	FE_RF2_1005_F00_F00	163
FE_RF2_101_F00_F180	105	VDD_RF1_P_FRI0	164	FE_RF2_1005_F00_F00	164
FE_RF2_101_F00_F180	106	VDD_RF1_P_FRI0	165	FE_RF2_1005_F00_F00	165
FE_RF2_101_F00_F180	107	VDD_RF1_P_FRI0	166	FE_RF2_1005_F00_F00	166
FE_RF2_101_F00_F180	108	VDD_RF1_P_FRI0	167	FE_RF2_1005_F00_F00	167
FE_RF2_101_F00_F180	109	VDD_RF1_P_FRI0	168	FE_RF2_1005_F00_F00	168
FE_RF2_101_F00_F180	110	VDD_RF1_P_FRI0	169	FE_RF2_1005_F00_F00	169
FE_RF2_101_F00_F180	111	VDD_RF1_P_FRI0	170	FE_RF2_1005_F00_F00	170
FE_RF2_101_F00_F180	112	VDD_RF1_P_FRI0	171	FE_RF2_1005_F00_F00	171
FE_RF2_101_F00_F180	113	VDD_RF1_P_FRI0	172	FE_RF2_1005_F00_F00	172
FE_RF2_101_F00_F180	114	VDD_RF1_P_FRI0	173	FE_RF2_1005_F00_F00	173
FE_RF2_101_F00_F180	115	VDD_RF1_P_FRI0	174	FE_RF2_1005_F00_F00	174
FE_RF2_101_F00_F180	116	VDD_RF1_P_FRI0	175	FE_RF2_1005_F00_F00	175
FE_RF2_101_F00_F180	117	VDD_RF1_P_FRI0	176	FE_RF2_1005_F00_F00	176
FE_RF2_101_F00_F180	118	VDD_RF1_P_FRI0	177	FE_RF2_1005_F00_F00	177
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FE_RF2_101_F00_F180	120	VDD_RF1_P_FRI0	179	FE_RF2_1005_F00_F00	179
FE_RF2_101_F00_F180	121	VDD_RF1_P_FRI0	180	FE_RF2_1005_F00_F00	180
FE_RF2_101_F00_F180	122	VDD_RF1_P_FRI0	181	FE_RF2_1005_F00_F00	181
FE_RF2_101_F00_F180	123	VDD_RF1_P_FRI0	182	FE_RF2_1005_F00_F00	182
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FE_RF2_101_F00_F180	126	VDD_RF1_P_FRI0	185	FE_RF2_1005_F00_F00	185
FE_RF2_101_F00_F180	127	VDD_RF1_P_FRI0	186	FE_RF2_1005_F00_F00	186
FE_RF2_101_F00_F180	128	VDD_RF1_P_FRI0	187	FE_RF2_1005_F00_F00	187
FE_RF2_101_F00_F180	129	VDD_RF1_P_FRI0	188	FE_RF2_1005_F00_F00	188
FE_RF2_101_F00_F180	130	VDD_RF1_P_FRI0	189	FE_RF2_1005_F00_F00	189
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FE_RF2_101_F00_F180	133	VDD_RF1_P_FRI0	192	FE_RF2_1005_F00_F00	192
FE_RF2_101_F00_F180	134	VDD_RF1_P_FRI0	193	FE_RF2_1005_F00_F00	193
FE_RF2_101_F00_F180	135	VDD_RF1_P_FRI0	194	FE_RF2_1005_F00_F00	194
FE_RF2_101_F00_F180	136	VDD_RF1_P_FRI0	195	FE_RF2_1005_F00_F00	195
FE_RF2_101_F00_F180	137	VDD_RF1_P_FRI0	196	FE_RF2_1005_F00_F00	196
FE_RF2_101_F00_F180	138	VDD_RF1_P_FRI0	197	FE_RF2_1005_F00_F00	197
FE_RF2_101_F00_F180	139	VDD_RF1_P_FRI0	198	FE_RF2_1005_F00_F00	198
FE_RF2_101_F00_F180	140	VDD_RF1_P_FRI0	199	FE_RF2_1005_F00_F00	199
FE_RF2_101_F00_F180	141	VDD_RF1_P_FRI0	200	FE_RF2_1005_F00_F00	200
FE_RF2_101_F00_F180	142	VDD_RF1_P_FRI0	201	FE_RF2_1005_F00_F00	201
FE_RF2_101_F00_F180	143	VDD_RF1_P_FRI0	202	FE_RF2_1005_F00_F00	202
FE_RF2_101_F00_F180	144	VDD_RF1_P_FRI0	203	FE_RF2_1005_F00_F00	203
FE_RF2_101_F00_F180	145	VDD_RF1_P_FRI0	204	FE_RF2_1005_F00_F00	204
FE_RF2_101_F00_F180	146	VDD_RF1_P_FRI0	205	FE_RF2_1005_F00_F00	205
FE_RF2_101_F00_F180	147	VDD_RF1_P_FRI0	206	FE_RF2_1005_F00_F00	206
FE_RF2_101_F00_F180	148	VDD_RF1_P_FRI0	207	FE_RF2_1005_F00_F00	207
FE_RF2_101_F00_F180	149	VDD_RF1_P_FRI0	208	FE_RF2_1005_F00_F00	208
FE_RF2_101_F00_F180	150	VDD_RF1_P_FRI0	209	FE_RF2_1005_F00_F00	209
FE_RF2_101_F00_F180	151	VDD_RF1_P_FRI0	210	FE_RF2_1005_F00_F00	210
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FE_RF2_101_F00_F180	153	VDD_RF1_P_FRI0	212	FE_RF2_1005_F00_F00	212
FE_RF2_101_F00_F180	154	VDD_RF1_P_FRI0	213	FE_RF2_1005_F00_F00	213
FE_RF2_101_F00_F180	155	VDD_RF1_P_FRI0	214	FE_RF2_1005_F00_F00	214
FE_RF2_101_F00_F180	156	VDD_RF1_P_FRI0	215	FE_RF2_1005_F00_F00	215
FE_RF2_101_F00_F180	157	VDD_RF1_P_FRI0	216	FE_RF2_1005_F00_F00	216
FE_RF2_101_F00_F180	158	VDD_RF1_P_FRI0	217	FE_RF2_1005_F00_F00	217
FE_RF2_101_F00_F180	159	VDD_RF1_P_FRI0	218	FE_RF2_1005_F00_F00	218
FE_RF2_101_F00_F180	160	VDD_RF1_P_FRI0	219	FE_RF2_1005_F00_F00	219
FE_RF2_101_F00_F180	161	VDD_RF1_P_FRI0	220	FE_RF2_1005_F00_F00	220
FE_RF2_101_F00_F180	162	VDD_RF1_P_FRI0	221	FE_RF2_1005_F00_F00	221
FE_RF2_101_F00_F180	163	VDD_RF1_P_FRI0	222	FE_RF2_1005_F00_F00	222
FE_RF2_101_F00_F180	164	VDD_RF1_P_FRI0	223	FE_RF2_1005_F00_F00	223
FE_RF2_101_F00_F180	165	VDD_RF1_P_FRI0	224	FE_RF2_1005_F00_F00	224
FE_RF2_101_F00_F180	166	VDD_RF1_P_FRI0	225	FE_RF2_1005_F00_F00	225
FE_RF2_101_F00_F180	167	VDD_RF1_P_FRI0	226	FE_RF2_1005_F00_F00	226
FE_RF2_101_F00_F180	168	VDD_RF1_P_FRI0	227	FE_RF2_1005_F00_F00	227
FE_RF2_101_F00_F180	169	VDD_RF1_P_FRI0	228	FE_RF2_1005_F00_F00	228
FE_RF2_101_F00_F180	170	VDD_RF1_P_FRI0	229	FE_RF2_1005_F00_F00	229
FE_RF2_101_F00_F180	171	VDD_RF1_P_FRI0	230	FE_RF2_1005_F00_F00	230
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FE_RF2_101_F00_F180	174	VDD_RF1_P_FRI0	233	FE_RF2_1005_F00_F00	233
FE_RF2_101_F00_F180	175	VDD_RF1_P_FRI0	234	FE_RF2_1005_F00_F00	234
FE_RF2_101_F00_F180	176	VDD_RF1_P_FRI0	235	FE_RF2_1005_F00_F00	235
FE_RF2_101_F00_F180	177	VDD_RF1_P_FRI0	236	FE_RF2_1005_F00_F00	236
FE_RF2_101_F00_F180	178	VDD_RF1_P_FRI0	237	FE_RF2_1005_F00_F00	237
FE_RF2_101_F00_F180	179	VDD_RF1_P_FRI0	238	FE_RF2_1005_F00_F00	238
FE_RF2_101_F00_F180	180	VDD_RF1_P_FRI0	239	FE_RF2_1005_F00_F00	239
FE_RF2_101_F00_F180	181	VDD_RF1_P_FRI0	240	FE_RF2_1005_F00_F00	240
FE_RF2_101_F00_F180	182	VDD_RF1_P_FRI0	241	FE_RF2_1005_F00_F00	241
FE_RF2_101_F00_F180	183	VDD_RF1_P_FRI0	242	FE_RF2_1005_F00_F00	242
FE_RF2_101_F00_F180	184	VDD_RF1_P_FRI0	243	FE_RF2_1005_F00_F00	243
FE_RF2_101_F00_F180	185	VDD_RF1_P_FRI0	244	FE_RF2_1005_F00_F00	244
FE_RF2_101_F00_F180	186	VDD_RF1_P_FRI0	245	FE_RF2_1005_F00_F00	245
FE_RF2_101_F00_F180	187	VDD_RF1_P_FRI0	246	FE_RF2_1005_F00_F00	246
FE_RF2_101_F00_F180	188	VDD_RF1_P_FRI0	247	FE_RF2_1005_F00_F00	247
FE_RF2_101_F00_F180	189	VDD_RF1_P_FRI0	248	FE_RF2_1005_F00_F00	248
FE_RF2_101_F00_F180	190	VDD_RF1_P_FRI0	249	FE_RF2_1005_F00_F00	249
FE_RF2_101_F00_F180	191	VDD_RF1_P_FRI0	250	FE_RF2_1005_F00_F00	250
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FE_RF2_101_F00_F180	193	VDD_RF1_P_FRI0	252	FE_RF2_1005_F00_F00	252
FE_RF2_101_F00_F180	194	VDD_RF1_P_FRI0	253	FE_RF2_1005_F00_F00	253
FE_RF2_101_F00_F180	195	VDD_RF1_P_FRI0	254	FE_RF2_1005_F00_F00	254
FE_RF2_101_F00_F180	196	VDD_RF1_P_FRI0	255	FE_RF2_1005_F00_F00	255
FE_RF2_101_F00_F180	197	VDD_RF1_P_FRI0	256	FE_RF2_1005_F00_F00	256
FE_RF2_101_F00_F180	198	VDD_RF1_P_FRI0	257	FE_RF2_1005_F00_F00	257
FE_RF2_101_F00_F180	199	VDD_RF1_P_FRI0	258	FE_RF2_1005_F00_F00	258
FE_RF2_101_F00_F180	200	VDD_RF1_P_FRI0	259	FE_RF2_1005_F00_F00	259
FE_RF2_101_F00_F180	201	VDD_RF1_P_FRI0	260	FE_RF2_1005_F00_F00	260
FE_RF2_101_F00_F180	202	VDD_RF1_P_FRI0	261	FE_RF2_1005_F00_F00	261
FE_RF2_101_F00_F180	203	VDD_RF1_P_FRI0	262	FE_RF2_1005_F00_F00	262
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FE_RF2_101_F00_F180	205	VDD_RF1_P_FRI0	264	FE_RF2_1005_F00_F00	264
FE_RF2_101_F00_F180	206	VDD_RF1_P_FRI0	265	FE_RF2_1005_F00_F00	265
FE_RF2_101_F00_F180	207	VDD_RF1_P_FRI0	266	FE_RF2_1005_F00_F00	266
FE_RF2_101_F00_F180	208	VDD_RF1_P_FRI0	267	FE_RF2_1005_F00_F00	267
FE_RF2_101_F00_F180	209	VDD_RF1_P_FRI0	268	FE_RF2_1005_F0	

A

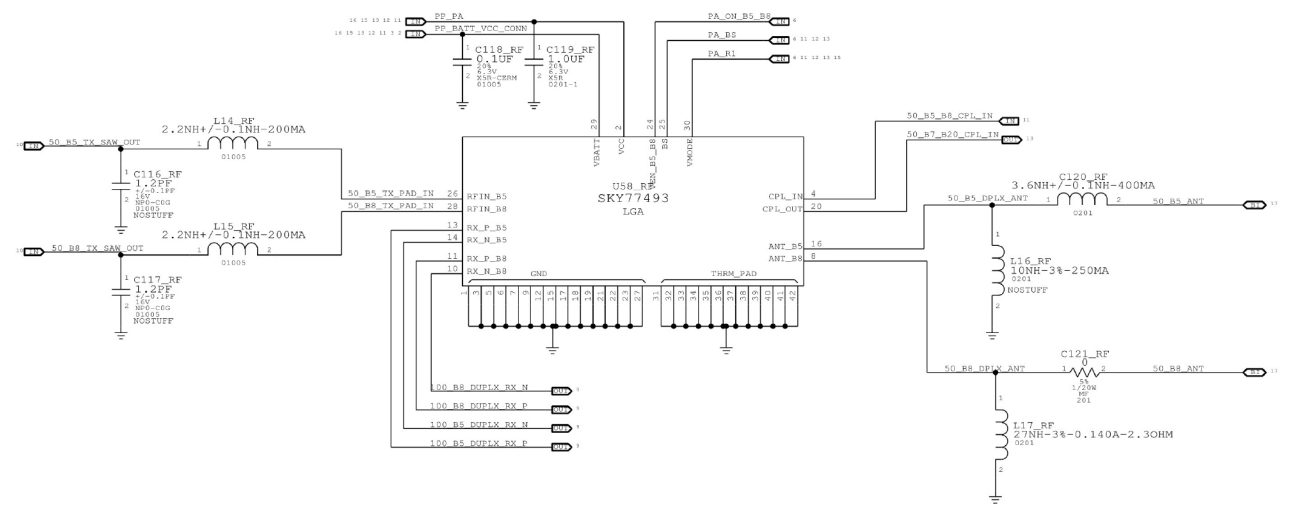
TX INTERSTAGE FILTERS

BAND 2/3 PAD



BAND	PA POWER MODE	PA_BS	PA_ON_B2_B3	PA_R1
OFF	X	X	0	X
B3	HPM	0	1	0
B3	LPM	0	1	1
B2	HPM	1	1	0
B2	LPM	1	1	1

BAND 5/8 PAD



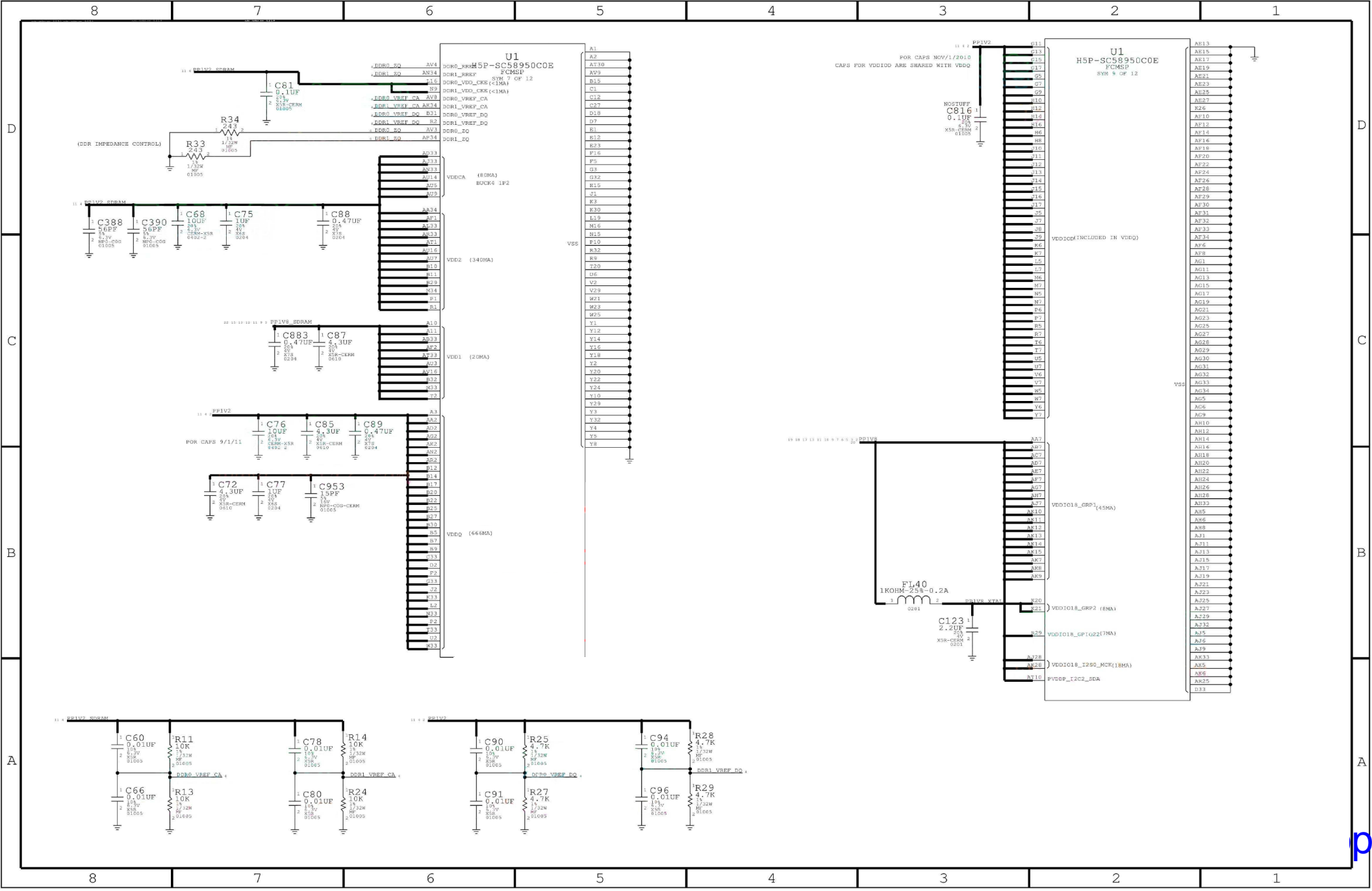
BAND	PA POWER MODE	PA_BS	PA_ON_B5_B8	PA_R1
OFF	X	X	0	X
B5	HPM	0	1	0
B5	LPM	0	1	1
B8	HPM	1	1	0
B8	LPM	1	1	1

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

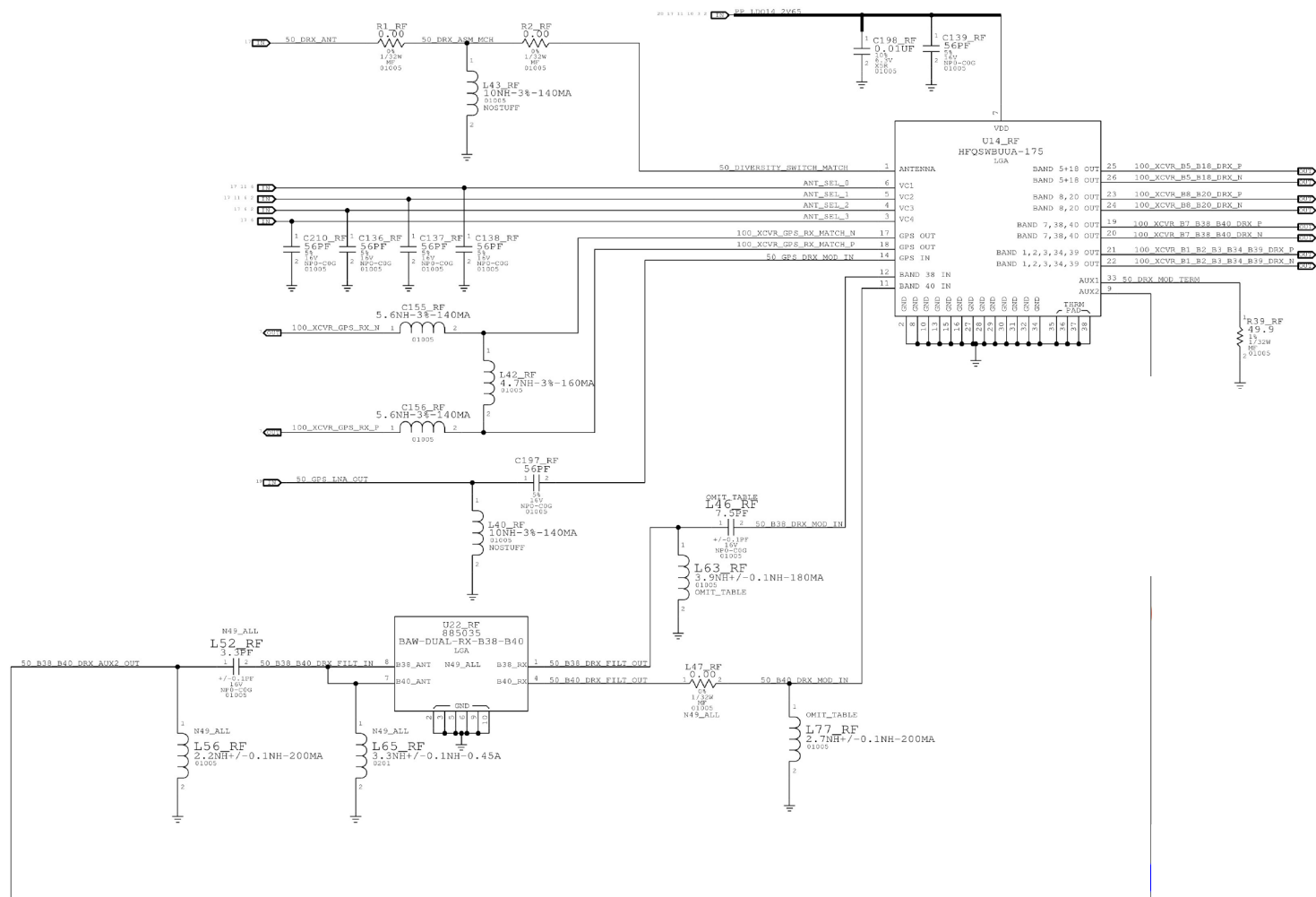
BAND	MODE	GAIN MODE	PA_R1	PCL RANGE
LOW BAND	LSM	ULTRA LOW	LOW	16 TO 15
LOW BAND	GSM	LOW	HIGH	7 TO 13
LOW BAND	GSM	MEDIUM	LOW	10 TO 15
LOW BAND	GSM	HIGH	LOW	5 TO 6
HIGH BAND	GSM	ULTRA LOW	HIGH	10 TO 15
HIGH BAND	GSM	LOW	HIGH	7 TO 9
HIGH BAND	GSM	HIGH	LOW	0 TO 6
LOW BAND	EDGE	LOW	LOW	15 TO 19
LOW BAND	EDGE	MEDIUM	LOW	10 TO 14
LOW BAND	EDGE	HIGH	LOW	8 TO 9
HIGH BAND	EDGE	LOW	HIGH	9 TO 15
HIGH BAND	EDGE	HIGH	LOW	2 TO 8

[illegible]

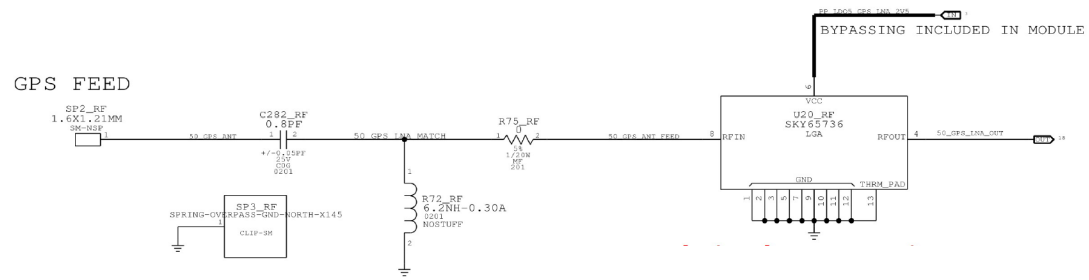
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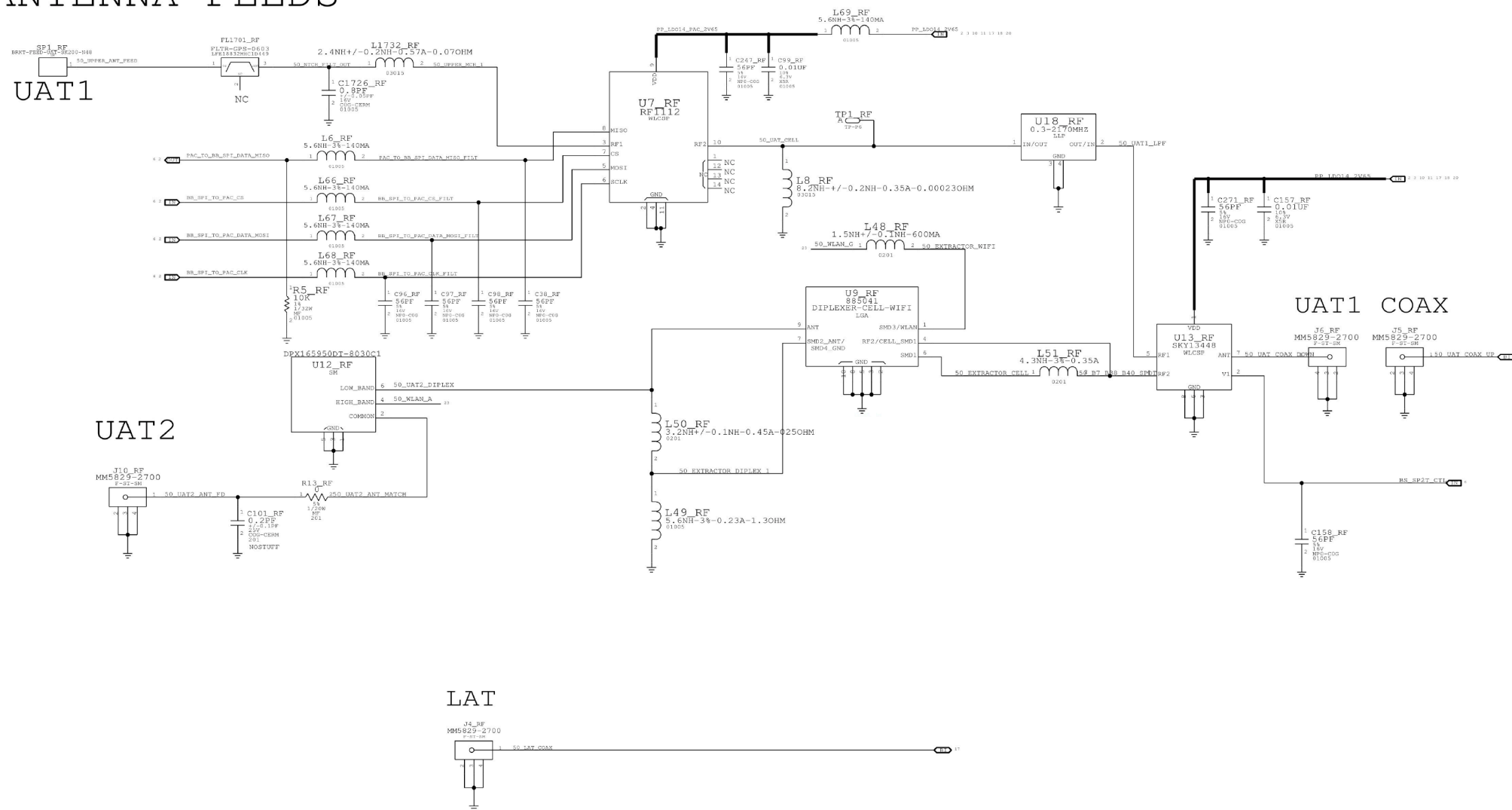
RX DIVERSITY

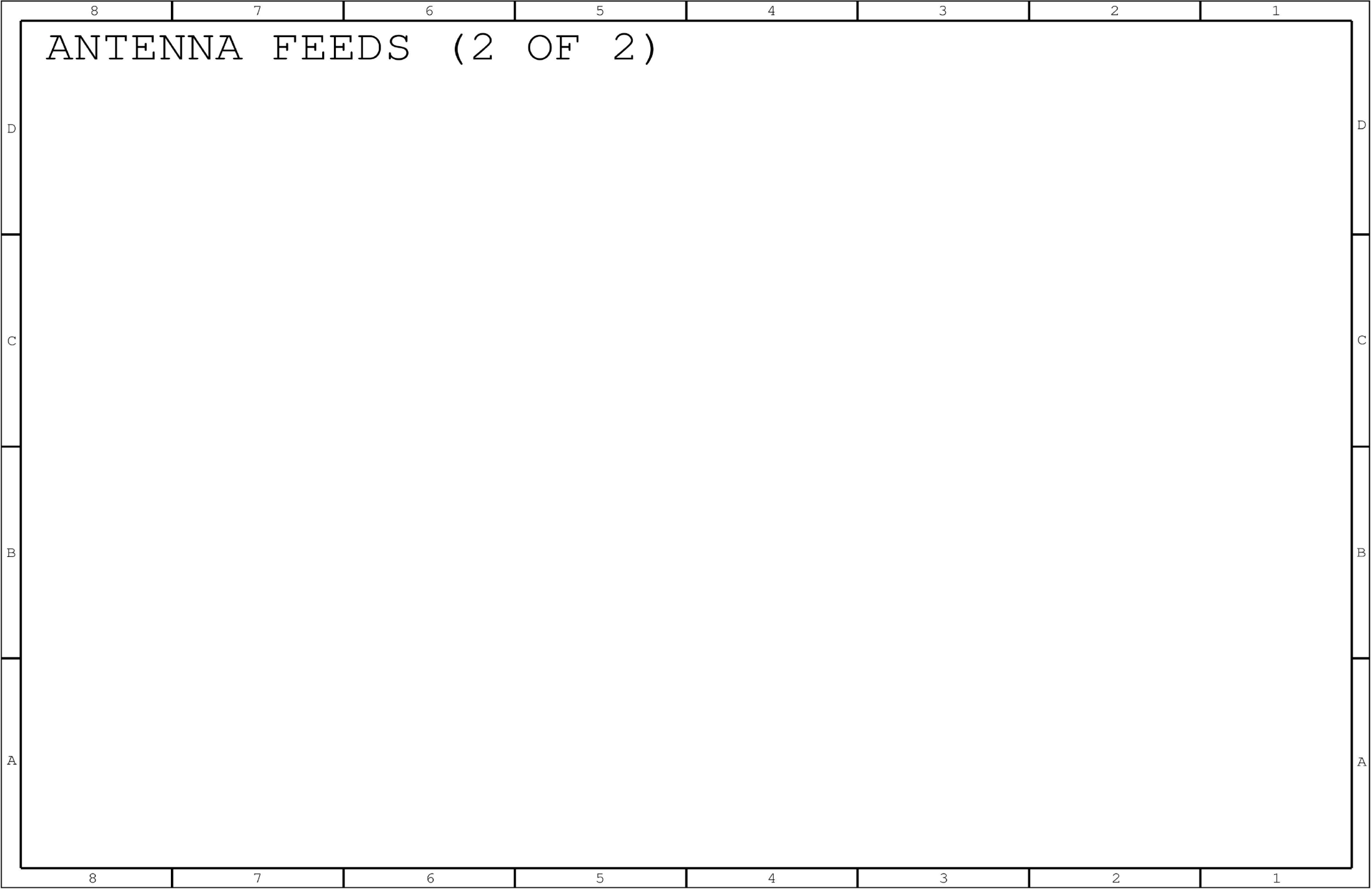


GPS



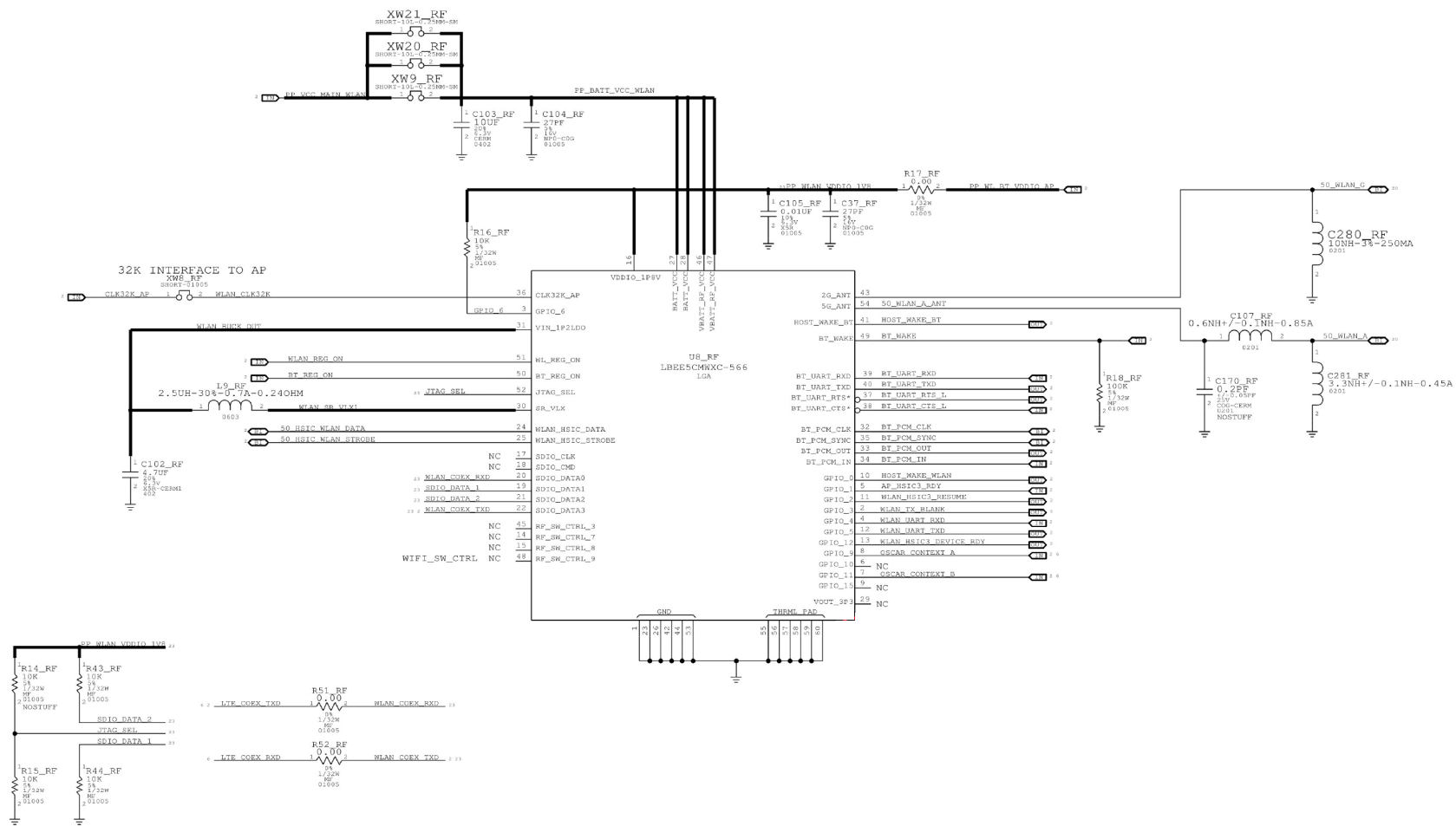
ANTENNA FEEDS

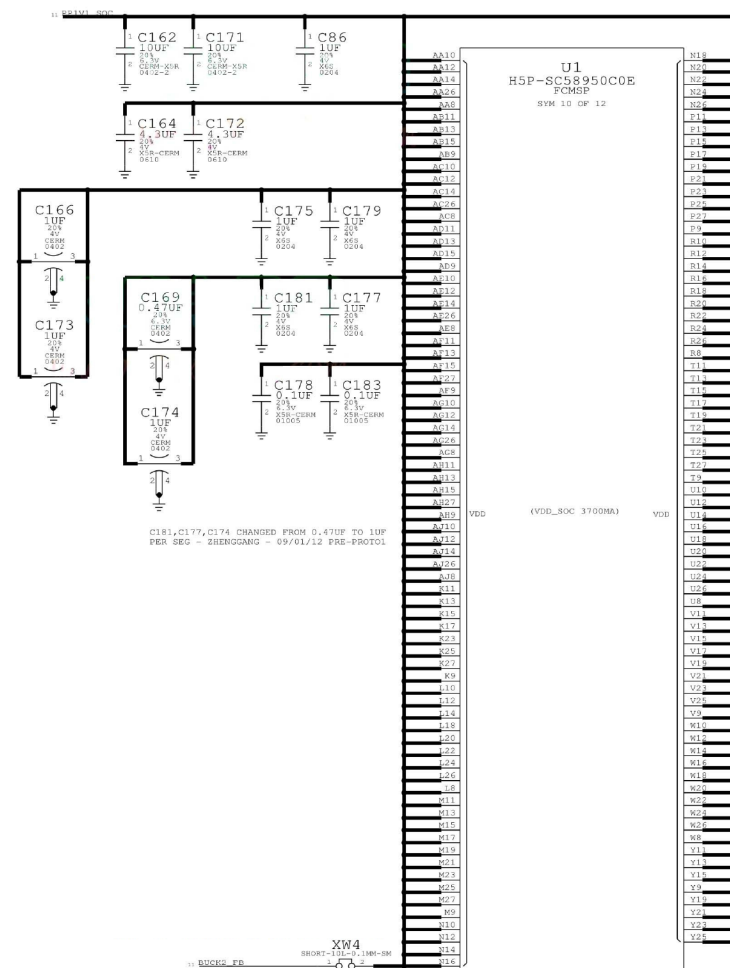
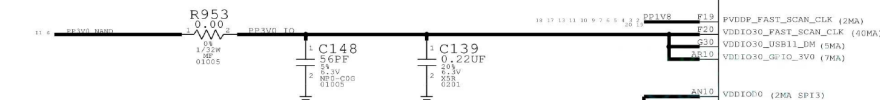
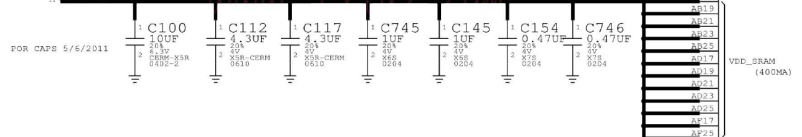
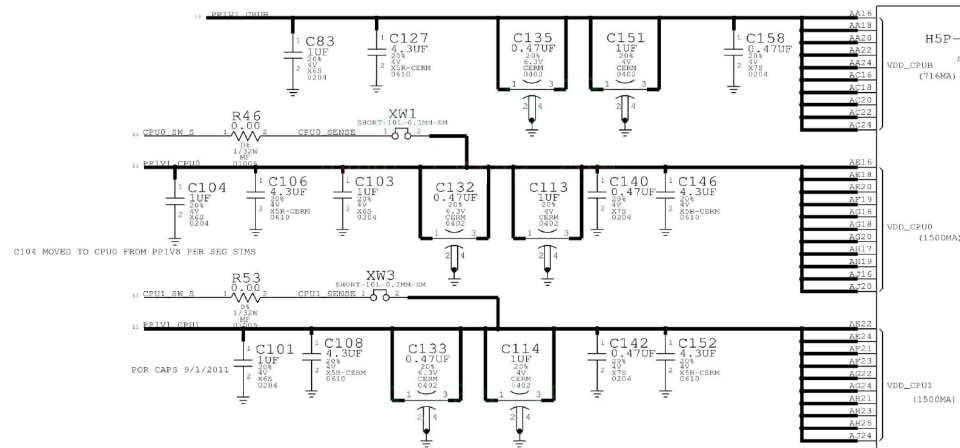




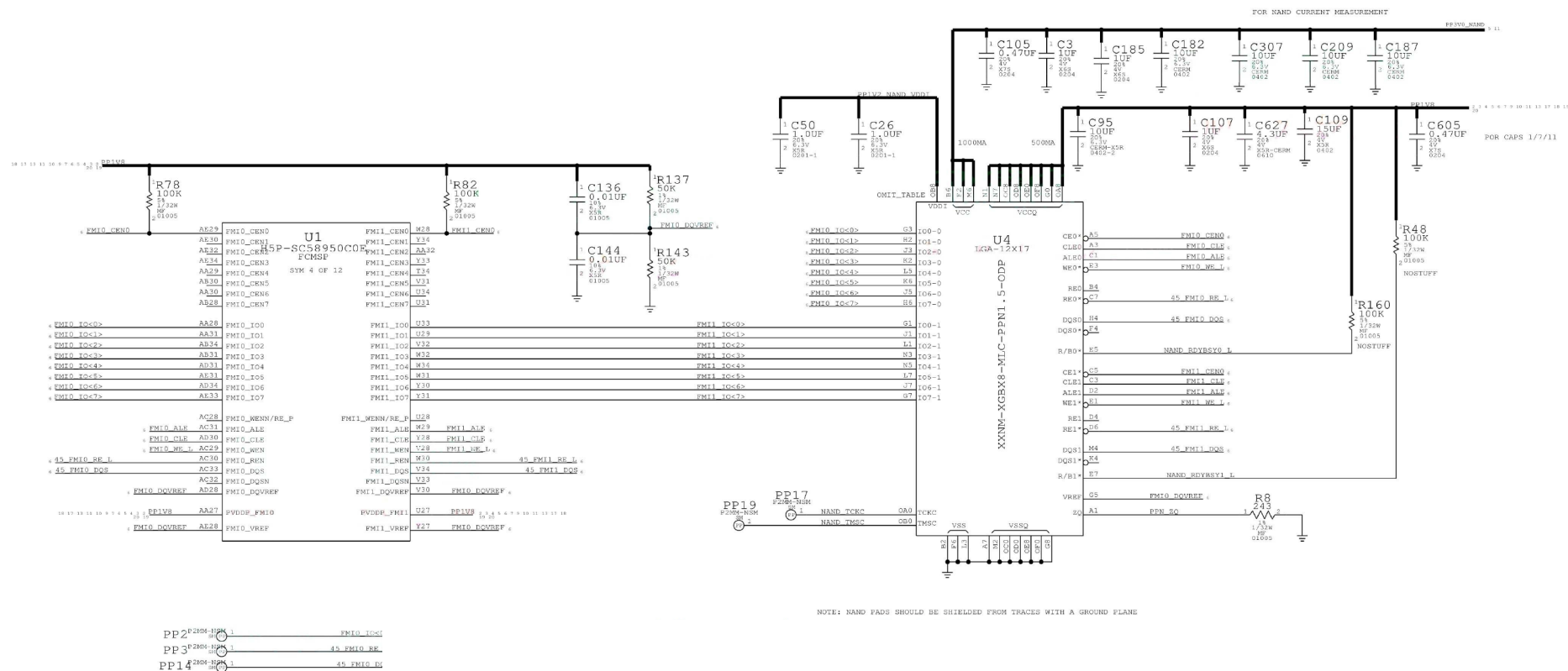
	8	7	6	5	4	3	2	1	
	FRONT END LOGIC TABLE								
	BAND	ANT_SEL_0	ANT_SEL_1	ANT_SEL_2	ANT_SEL_3	ANT_SEL_4	PRX PATH	DRX PATH	
D	GSM LB TX	LOW	HIGH	LOW	LOW	LOW	LAT	TERMINATED	
	GSM LB TX	LOW	HIGH	LOW	LOW	HIGH	UAT	TERMINATED	
	GSM HB TX	HIGH	HIGH	LOW	HIGH	LOW	LAT	TERMINATED	
	GSM HB TX	HIGH	HIGH	LOW	HIGH	HIGH	UAT	TERMINATED	
C	B1	HIGH	HIGH	HIGH	HIGH	LOW	LAT	UAT	
	B1	HIGH	HIGH	HIGH	HIGH	HIGH	UAT	LAT	
	B2/B25, 1900RX	HIGH	LOW	LOW	HIGH	LOW	LAT	UAT	
	B2/B25, 1900RX	HIGH	LOW	LOW	HIGH	HIGH	UAT	LAT	
	B3	HIGH	HIGH	LOW	LOW	LOW	LAT	UAT	
	B3	HIGH	HIGH	LOW	LOW	HIGH	UAT	LAT	
	B5/B6/B18, 850RX	HIGH	LOW	LOW	LOW	LOW	LAT	UAT	
	B5/B6/B18, 850RX	HIGH	LOW	LOW	LOW	HIGH	UAT	LAT	
	B20	HIGH	LOW	HIGH	HIGH	LOW	LAT	UAT	
	B20	HIGH	LOW	HIGH	HIGH	HIGH	UAT	LAT	
	B34/B39 TX	LOW	LOW	HIGH	HIGH	LOW	LAT	TERMINATED	
	B34/B39 TX	LOW	LOW	HIGH	HIGH	HIGH	UAT	TERMINATED	
	B34 RX	LOW	LOW	LOW	HIGH	LOW	LAT	UAT	
	B34 RX	LOW	LOW	LOW	HIGH	HIGH	UAT	LAT	
	B39 RX	LOW	LOW	HIGH	LOW	LOW	LAT	UAT	
	B39 RX	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT	
	B38/B40 TX	LOW	HIGH	HIGH	LOW	LOW	LAT	TERMINATED	
	B38/B40 TX	LOW	HIGH	HIGH	LOW	HIGH	UAT	TERMINATED	
	B38 RX	HIGH	LOW	HIGH	LOW	LOW	LAT	UAT	
	B38 RX	HIGH	LOW	HIGH	LOW	HIGH	UAT	LAT	
	B40 RX	HIGH	HIGH	HIGH	LOW	LOW	LAT	UAT	
A	B40 RX	HIGH	HIGH	HIGH	LOW	HIGH	UAT	LAT	
	B7	LOW	HIGH	HIGH	HIGH	LOW	LAT	UAT	
	B7	LOW	HIGH	HIGH	HIGH	HIGH	UAT	LAT	
	B8, GSM900 RX	LOW	HIGH	LOW	HIGH	LOW	LAT	UAT	
	B8, GSM900 RX	LOW	HIGH	LOW	HIGH	HIGH	UAT	LAT	
	GSM1800 RX	LOW	LOW	LOW	LOW	LOW	LAT	TERMINATED	
	GSM1800 RX	LOW	LOW	LOW	LOW	HIGH	UAT	TERMINATED	
	LAT = LOWER ANTENNA UAT = UPPER ANTENNA								
	8	7	6	5	4	3	2	1	

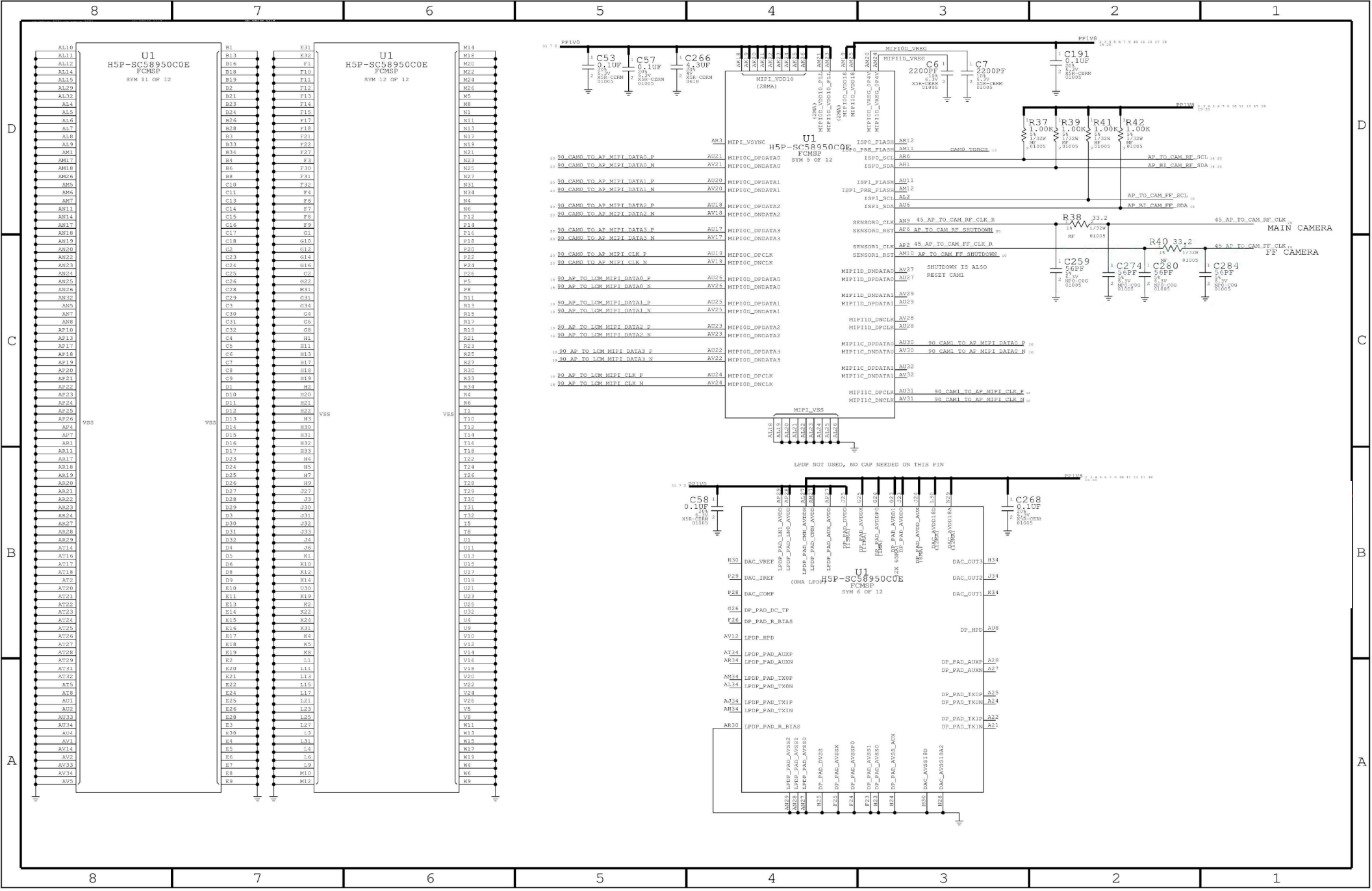
WLAN/BT





NAND





8
 7
 6
 5
 4
 3
 2
 1

BUTTON FLEX (VIBE DRIVER, BUTTONS, ANC REF MIC, STROBE, STROBE_NTC)

